

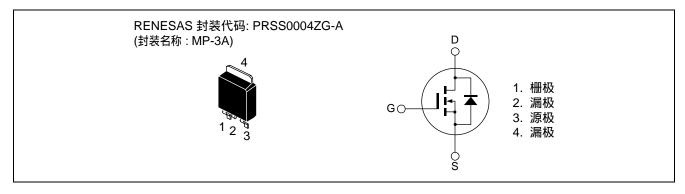
RJK5002DPD

500V - 2.4A - 场效应晶体管 快速电源开关 R07DS0866CJ0100 修订版本 1.00 Nov 05, 2012

特点

- 低漏极/源极通态电阻
 R_{DS(on)} = 3.83 Ω 典型值 (I_D = 1.2 A, V_{GS} = 10 V, Ta = 25°C)
- 快速开关时间

封装形式



绝对最大额定值

 $(Ta = 25^{\circ}C)$

参数	符号	额定值	单位
漏极/源极电压	V _{DSS}	500	V
栅极/源极电压	V _{GSS}	±30	V
漏极电流	l _D ^{i±1}	2.4	А
脉冲漏极电流	I _{D(pulse)} 注2	4.8	А
体二极管反向漏极电流	I _{DR} ^{注1}	2.4	А
体二极管反向脉冲漏极电流	I _{DR(pulse)} 注2	4.8	А
雪崩电流	I _{AP} ^{注3}	2.4	А
沟道最大容许损耗	Pch ^{注 4}	30	W
沟道-外壳间热阻	θch-c	4.17	°C/W
沟道温度	Tch	150	°C
储存温度	Tstg	-55 to +150	°C

- 注: 1. 限于 Tch 的最大值
 - 2. 脉宽限于安全工作区域
 - 3. STch = 25° C, Tch $\leq 150^{\circ}$ C
 - 4. 在 Tc = 25°C 的容许值

电特性

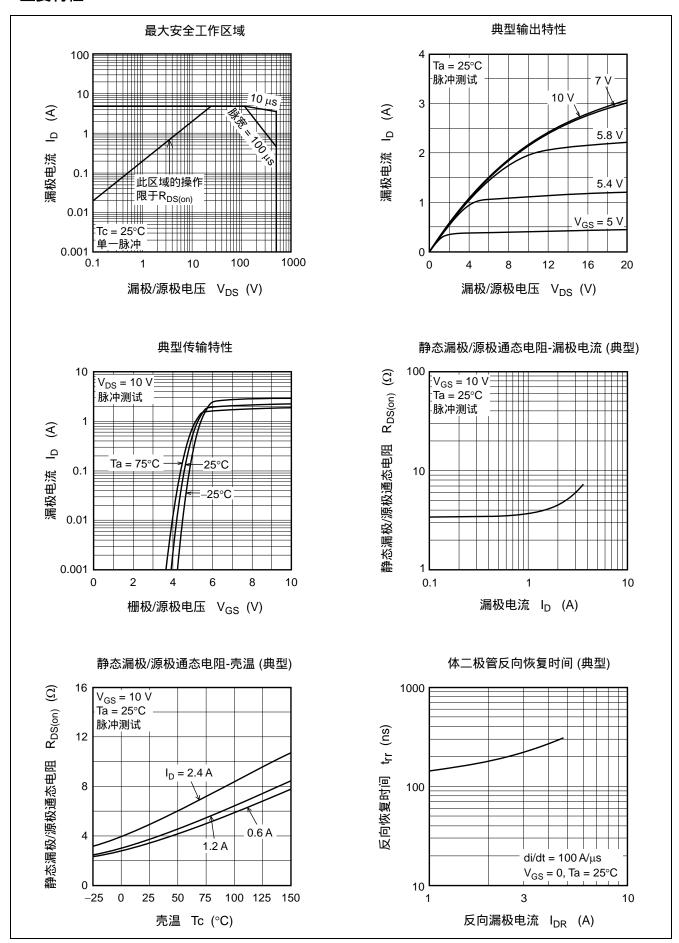
 $(Ta = 25^{\circ}C)$

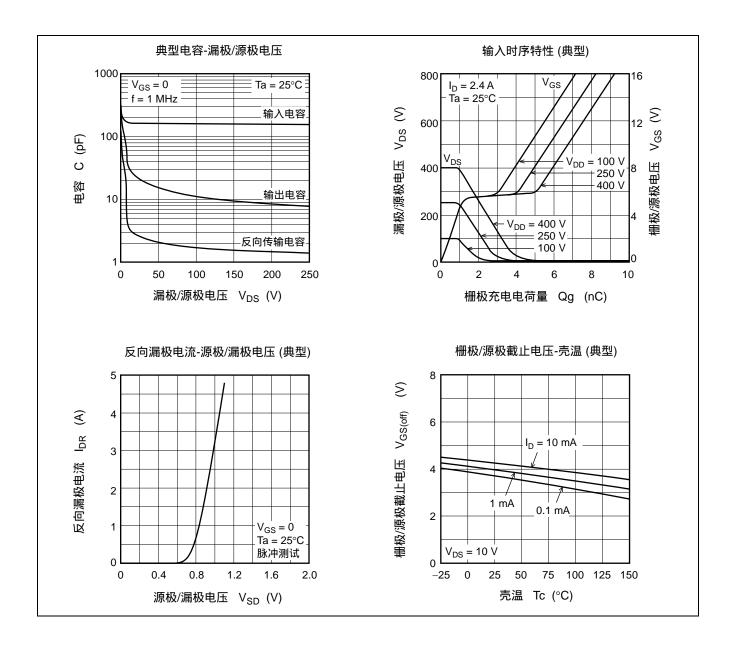
参数	符号	最小值	典型值	最大值	单位	测定条件
漏极/源极破坏电压	$V_{(BR)DSS}$	500	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
漏极截止电流	I_{DSS}			1	μΑ	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
栅极截止电流	I_{GSS}		_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
栅极/源极截止电压	$V_{GS(off)}$	3.5	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
静态漏极/源极通态电阻	R _{DS(on)}		3.83	5.00	Ω	$I_D = 1.2 \text{ A}, V_{GS} = 10 \text{ V}^{\pm 5}$
输入电容	Ciss		165	1	pF	V _{DS} = 25 V
输出电容	Coss		21	_	pF	$V_{GS} = 0$
反向传输电容	Crss	_	2.6	_	рF	f = 1 MHz
接通延迟时间	t _{d(on)}	_	11	_	ns	I _D = 1.2 A
上升时间	t _r	_	12	_	ns	$V_{GS} = 10 \text{ V}$
关断延迟时间	t _{d(off)}	_	22	_	ns	$R_L = 208 \Omega$
下降时间	t _f	_	22	_	ns	$Rg = 10 \Omega$
栅极充电电荷量	Qg	_	6.7	_	nC	V _{DD} = 400 V
栅极/源极充电电荷量	Qgs	_	1.3	_	nC	$V_{GS} = 10 \text{ V}$
栅极/漏极充电电荷量	Qgd	_	3.8	_	nC	$I_D = 2.4 \text{ A}$
体二极管正向电压	V_{DF}	_	0.9	1.5	V	I _F = 2.4 A, V _{GS} = 0 ^{注 5}
体二极管反向恢复时间	t _{rr}	_	235	_	ns	$I_F = 2.4 \text{ A}, V_{GS} = 0$
						$V_{DD} = 400 \text{ V}$
						$di_F/dt = 100 A/\mu s$

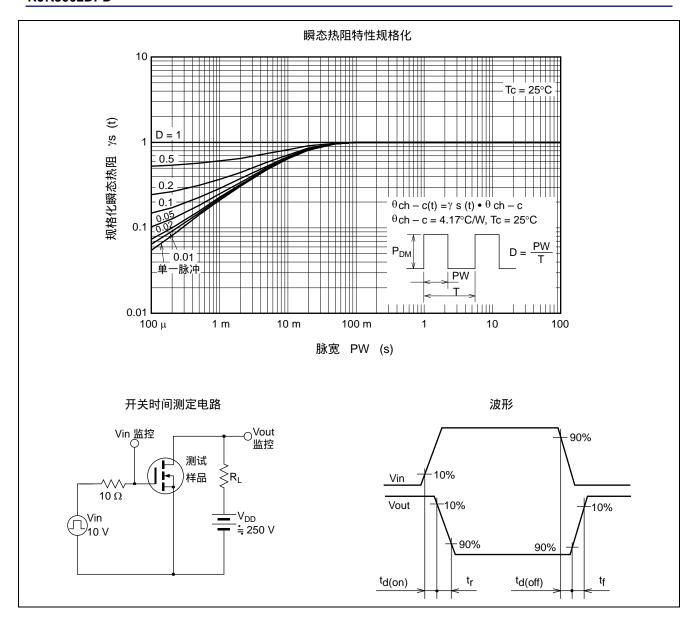
注: 5. 脉冲测试

- 6. 由于此器件配备高电压 FET 芯片 ($V_{DSS} \ge 500~V$), 含高电压供给。因此,请务必确认漏极终端与其他终端之间的静电放电。
- 7. 此为静电敏感器件,请谨慎处理此产品。

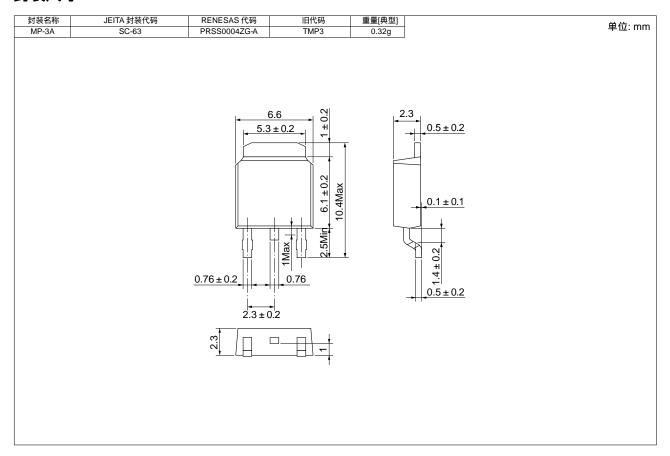
主要特性







封装尺寸



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订购型号	数量	运输包装
RJK5002DPD-00#J2	3000 枚	带卷包装

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Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-651-709, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No. 1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tei: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2886-9318, Fax: +852 2886-9022/9044

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Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949 Tel: +65-6213-0200. Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050
Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

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