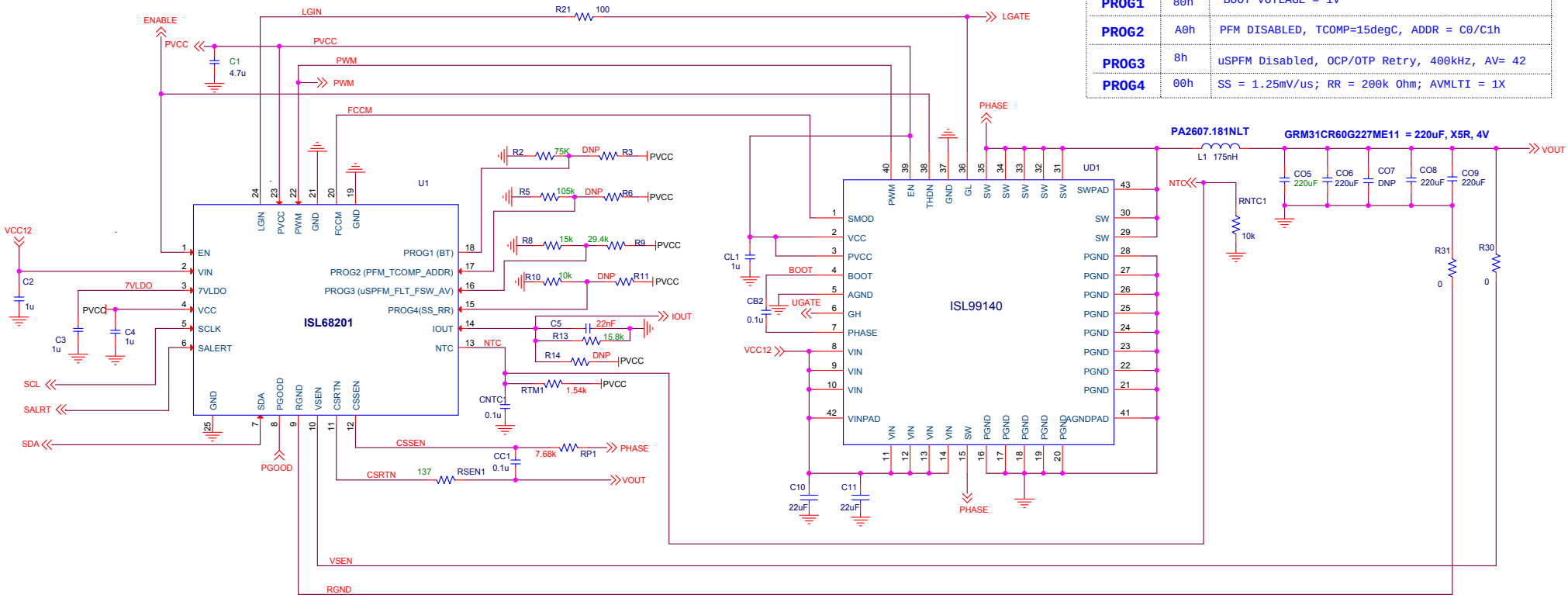
RESISTOR READER

PROG1	80h	BOOT VOTLAGE = 1V
PROG2	A0h	PFM DISABLED, TCOMP=15degC, ADDR = C0/C1h
PROG3	8h	uSPFM Disabled, OCP/OTP Retry, 400kHz, AV= 42
PROG4	00h	SS = 1.25mV/us; RR = 200k Ohm; AVMLTI = 1X



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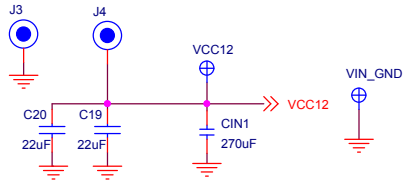
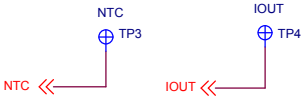
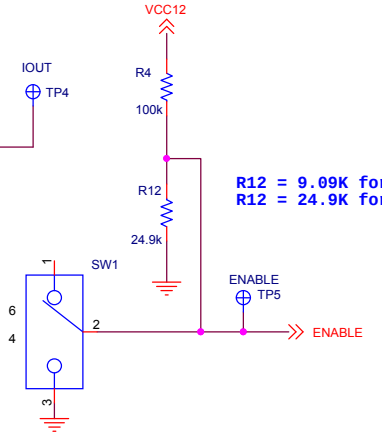
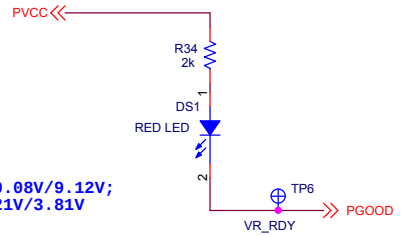






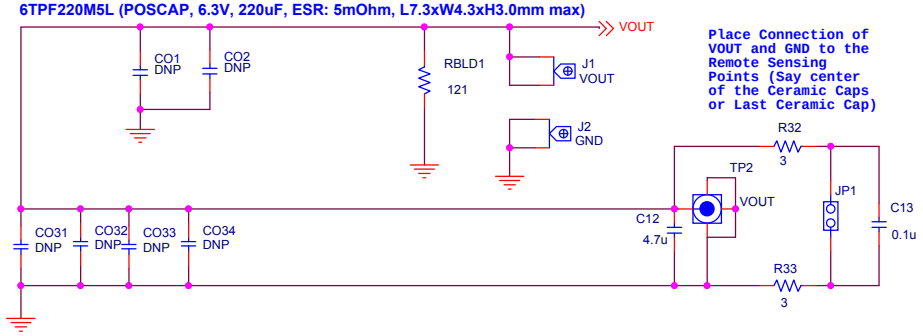
R12 = 9.09K for typical POR = 10.08V/9.12V;
R12 = 24.9K for typical POR = 4.21V/3.81V



VOUT PGOOD

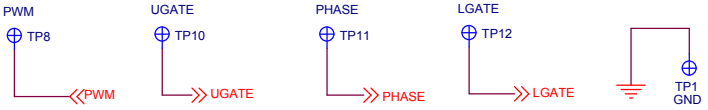
LED LIGHT	ON	OFF
VOUT STATUS	DOWN	UP

6TPF220M5L (POSCAP, 6.3V, 220uF, ESR: 5mOhm, L7.3xW4.3xH3.0mm max)



Place Connection of VOUT and GND to the Remote Sensing Points (Say center of the Ceramic Caps or Last Ceramic Cap)

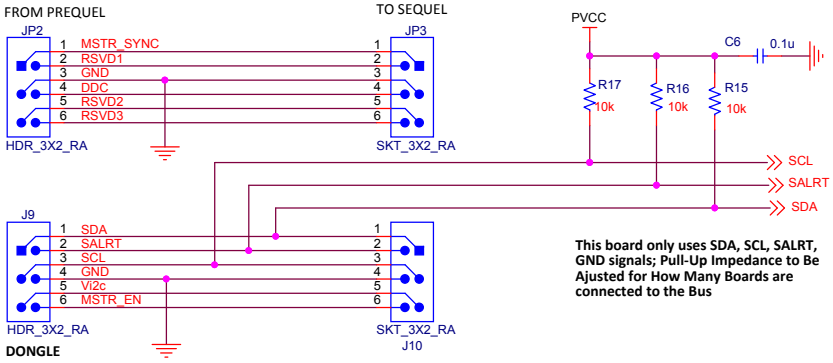
DNP these and leave the holes for probes



BOARD TO BOARD INTERFACE

ON LEFT OF BOARD (MALE)

ON RIGHT OF BOARD (FEMALE)



This board only uses SDA, SCL, SALRT, GND signals; Pull-Up Impedance to Be Ajusted for How Many Boards are connected to the Bus

SALRT AND SDA SHORTED DUE TO SCHEMATIC wiring MISTAKE in REV A, which is the only difference in between REVA and REVB schematics and layout

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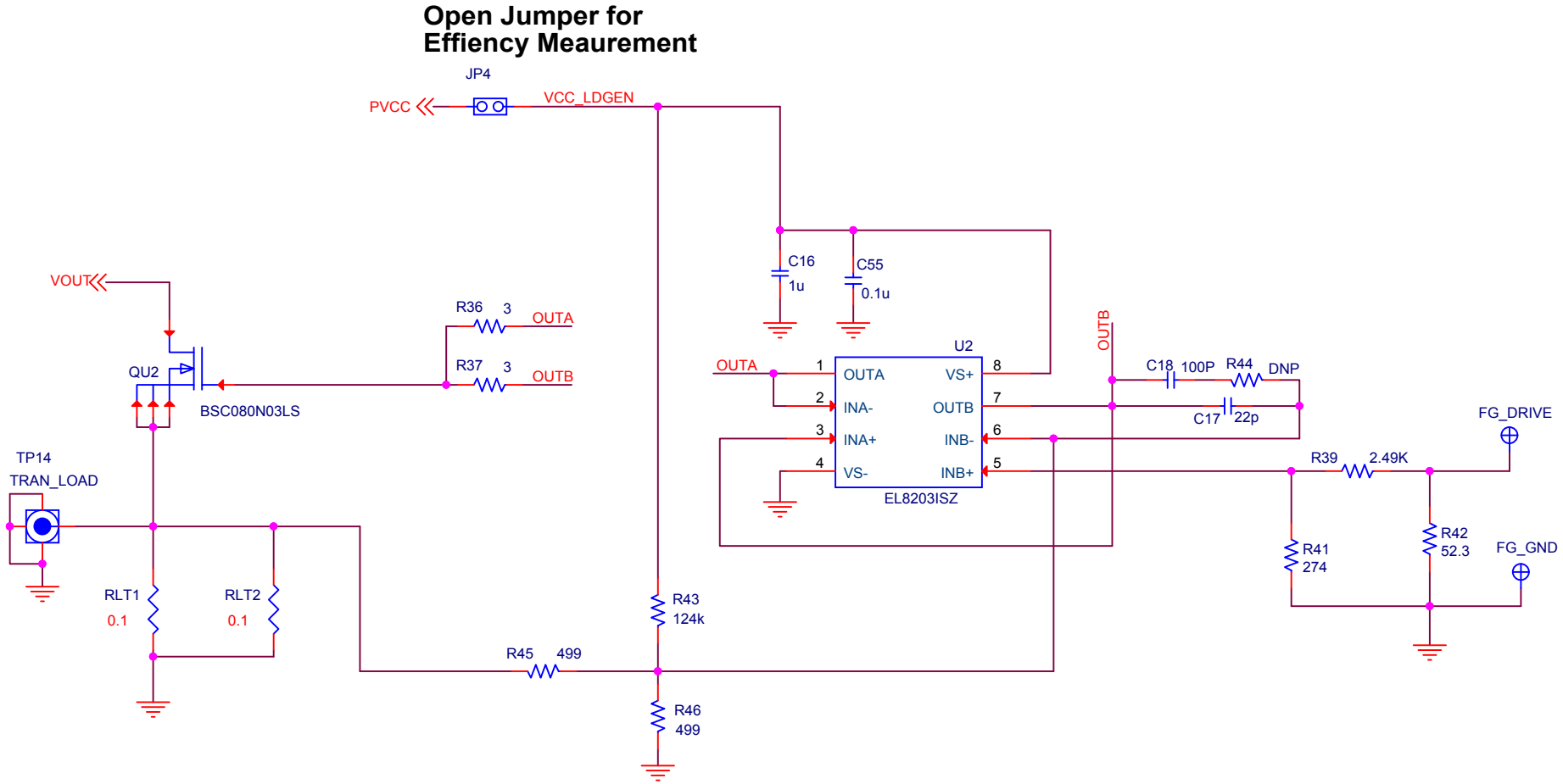
On-Board Load Transient



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