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April 1st, 2010 Renesas Electronics Corporation

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M16C/80 Group

Operation of Timer A (event counter mode, reload type)

1.0 Abstract

In timer mode, choose functions from those listed in Table 1. Operations of the circled items are described below

Table 1. Choosed functions

Item		Set-up	Item	Set-up	
Count source		Input signal to TAilN (counting falling edges)	Pulse output function	0	No pulses output
	0				Pulses output
		Input signal to TAiiN (counting rising edges)	Count operation type	0	Reload type
					Free-run type
		Timer overflow (TB2/TAj overflow)	Factor for switching between up and down	0	Content of up/down flag
					Input signal to TAiouT

Note: j = i - 1, but j = 4 when i = 0.

2.0 Introduction

Operation (1) Setting the count start flag to "1" causes the counter to count the falling edges of the count source.

- (2) If an underflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to "1".
- (3) If switching from an up count to a down count or vice versa while a count is in progress, the switch takes effect from the next effective edge of the count source.
- (4) Setting the count start flag to "0" causes the counter to hold its value and to stop.
- (5) If an overflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to "1".

Note

• When not using pulse output, do not select TAiouT output function with the function select register A and B.

Figure 1 shows the operation timing

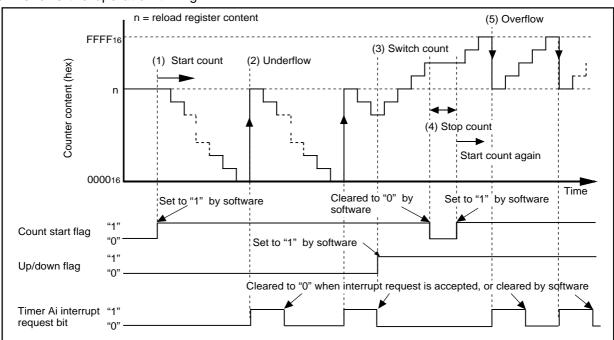
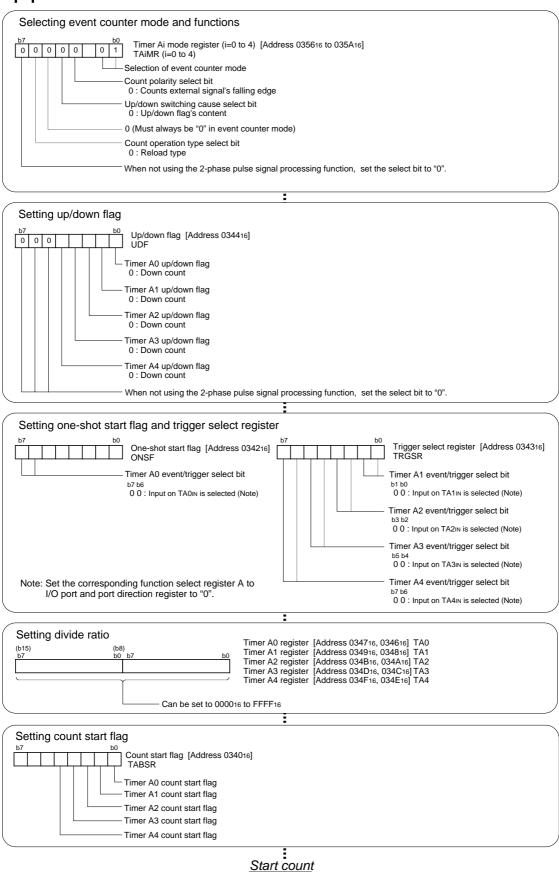


Figure 1. Operation timing of event counter mode, reload type



3.0 Set-up procedure





4.0 Programming Code

```
M16C/80 Program Collection
 FILE NAME : rjj05b0125_src.a30
 CPU : M16C/80 Group
 FUNCTION : Operation of Timer A
        (event counter mode, reload type)
 HISTORY : 2003.06.16 Ver 1.00
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.LIST OFF ;Stops outputting lines to the assembler list file
    .INCLUDE sfr80100.inc ; Reads the file that defined SFR
                 ;Starts outputting lines to the assembler list file
Symbol definition
ROM_TOP .EQU OFFC000H ;Start address of ROM
FIXED_VECT_TOP .EQU OFFFFDCH ;Start address of fixed vector
      .EQU (08000H-1H); Counter value on event counter mode (down count)
C_CNT_TA_EV
Program area
.SECTION PROGRAM, CODE ; Declares section name and section type.
         ROM_TOP
                 ;Declares start address.
RESET:
    ; Sets Processor mode, System clock and Main clock division
        #03H, prcr
                  Removes protect
        #10000000B, pm0 ; Single-chip mode
    MOV.B
    MOV.B #11000000B, pm1 ; Flash memory version
    MOV.B #00001000B, cm0 ; Xcin-Xcout High
    MOV.B #00100000B, cml; Xin-Xout High
    MOV.B #00010010B, mcd ; No division mode
    MOV.B #00H, prcr ;Protects all registers
```



Operation of Timer A (event counter mode, reload type)

```
TimerA (event counter mode, reload type selected)
; Setting event counter mode and functions
            #00000001B, ta0mr
              |||||++----;Selection of event counter mode
              |||||+----;This bit is invalid in M16C/80 series
               |||+----;Count polarity select bit
                           (0:Counts external signal's falling edge)
              |||+----:Up/down switching cause select bit (0:Up/down flag's content)
              | | +----; Must always be "0" in event counter mode
              +-----;Count operation type select bit (0:Reload type)
              +----;When not using the 2-phase pulse signal processing function,
                           set the select bit to "0"
      ; Setting up/down flag
             #0000000B, udf
      MOV.B
             +----;TimerA0 up/down flag (0:Down count)
                  ----:When not using the 2-phase pulse signal processing function,
                           set the select bit to "0"
      ; Setting one-shot start flag and trigger select register
            #0000000B, onsf
             ++----;Timer A0 event/trigger select bit
                           (00:Input on TAOIN is selected) (Note)
      ; (Note) Set the corresponding function select register A to I/O port
      ; and port direction register to "0"
                  Port P71 direction register
           pd7_1
      BCLR
      BCLR
            ps1 1
                          ;Port P71 is I/O port
      ; Setting divide ratio
      MOV.W #C_CNT_TA_EV, ta0
      ; Setting counter start flag
      MOV.B #0000001B, tabsr
                   +----;Timer A0 count start flag
MAIN:
     JMP
            MAIN
Dummy interrupt processing program
dummy:
     REIT
Setting of fixed vector
.SECTION F_VECT, ROMDATA
             FIXED_VECT_TOP
      .LWORD
            dummy
                    ;Undefined instruction
      .LWORD
            dummy
                    ;Overflow
      .LWORD
             dummy
                    ;BRK instruction execution
      .LWORD
             dummy
                    ;Address match
      .LWORD
             dummy
             dummy
      .LWORD
                    ;Watchdog timer
      .LWORD
             dummy
      .LWORD
             dummy
                    ;NMI
      .LWORD
             RESET
                    ;Reset
      .END
```

5.0 Reference

Renesas Technology Corporation Semiconductor Home page

http://www.renesas.com/

Technical Support

E-mail: support_apl@renesas.com

Data Sheet

M16C/80 group Rev. E3 (Use the latest version on the Home page: http://www.renesas.com/)

User's Manual

M16C/80 group Rev. B (Use the latest version on the Home page: http://www.renesas.com/)

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