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H8SX Series

W/L Size Immediate Abbreviation

Introduction

As well as having an architecture that is upward-compatible with each CPU of the H8/300, H8/300H, and H8S series, so as to inherit a full complement of peripheral functions, the H8SX microcomputer series has a maximum operating frequency of 50 MHz and uses a 32-bit H8SX core CPU as well as an on-chip multiplier/divider to improve performance.

This H8SX series Application Note provides information you may be need during software and hardware design. This is a basic edition that provides operation examples that each use a single H8SX series on-chip peripheral function.

Although the operation of each program, circuit, and other aspects covered by this application note has been checked, make sure that you conduct your own operation checks before actually using the H8SX series.

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1. Overview

The H8SX series has an architecture that is upward-compatible with each CPU of the H8/300, H8/300H, and H8S series. Furthermore, in addition its instruction set has been enhanced to improve CPU performance. This enhancement of the instruction set has greatly improved coding efficiency compared to the conventional series. This coding efficiency leads to benefits such as a reduction in the amount of ROM required for storing programs as well as the shortening of each instruction fetch cycle. This application note describes "W/L size immediate abbreviation", which is an enhanced instruction set item.

2. Configuration

"W/L size immediate abbreviation" is described below. The conventional H8/300, H8/300H, and H8S series support :8, :16, and :32 as their size immediate abbreviations. For the H8SX series, :3 and :4 are also supported. For example, to set immediate values 0 to 7 in the short variable with the conventional H8S series, :16 is used; with the H8SX series, :3 is used. With the H8SX series, there is no need to allocate a 16-bit area in the program code, which reduces the size of the program. An example is shown in Figure 1.

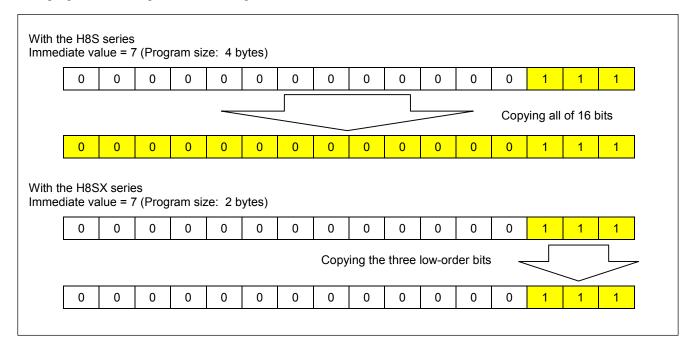


Figure 1 Immediate Abbreviations

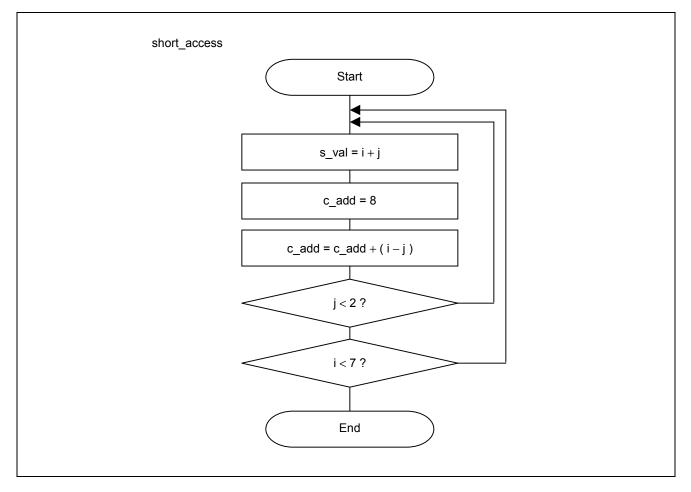


3. Sample Program

3.1 Flowchart

The sample program shown below is very simple, and will allow you to understand the description of "W/L size immediate abbreviation", an enhanced instruction set item.

As a comparison with the H8S series, the results of compilation are shown. This example is for reference only because the instruction code length generated in the compilation of an application-level program greatly depends on the source program and compiling conditions.





3.2 Program Listing

```
/* Include File
                                */
#include <machine.h>
void short access(void);
/* RAM allocation
                                */
short s val;
                // Short Data
volatile char c_add; // char Data
/* Function Definition(Main Program)
                                */
void short access(void)
{
 unsigned short i,j;
for( i=0; i<7; i++ )
 unsigned short i,j;
                // Loop Counter
                // i Loop
 {
   for( j=0; j<2; j++)
                // j Loop
   {
     s_val = i + j;
                // i + j
     c_add = 8;
                // c_add Initialize
     c add +=(i-j);
   }
 }
}
```



3.3 Comparison of the H8S Series with the H8SX Series

The result of compilation (assembly code) with the H8S series is shown below.

Ρ				;	section	
	00000000	short access:		;	function	: short_access
	00000000	STM.L	(ER4-ER5),@-SP			—
	00000004	MOV.L	# s val,ER5			
	A000000A	SUB.W	 E4,E4			
	000000C	MOV.L	#_c_add,ER1			
	00000012	L70:				
	00000012	SUB.W	E0,E0			
	00000014	L71:				
	00000014	MOV.W	E4,R0			
	00000016	ADD.W	E0,R0			
	00000018	MOV.W	R0,@ER5			
	000001A	MOV.B	#8,R0L			
	0000001C	MOV.B	R0L,@_c_add:32			
	00000022	MOV.W	E4,R0			
	00000024	SUB.W	E0,R0			
	00000026	MOV.B	@ER1,ROH			
	00000028	ADD.B	ROL,ROH			
	0000002A	MOV.B	ROH,@ER1			
	0000002C	INC.W	#1,E0			
	0000002E	CMP.W	#2,E0			
	0000032	BLO	L71:8			
	0000034	INC.W	#1,E4			
	00000036	CMP.W	#7,E4			
	000003A	BLO	L70:8			
	000003C	LDM.L	@SP+,(ER4-ER5)			
	00000040	RTS				
В		_			section	_
	00000000		_	;	static:	s_val
	00000000	.RES.W	1			
	00000002		-	;	static:	c_add
	00000002	.RES.B	1			

The result of compilation (assembly code) with the H8SX series is shown below.

Р			; section
00000000	_short_access:		; function: short_access
00000000	PUSH.W	R3	
0000002	SUB.W	R1,R1	
0000004	L8:		
0000004	SUB.W	E0,E0	
0000006	MOV.B	R1L,R0L	
0000008	L10:		
0000008	MOV.W	E0,R0	
000000A	ADD.W	R1,R0	
000000C	MOV.W	R0,E1	
000000E	MOV.B	#8:4,@_c_add:32	
00000014	MOV.B	R1L,R0L	
00000016	MOV.W	E0,R3	
00000018	MOV.B	R3L,R0H	



000001A	SUB.B	R3L,R0L	
0000001C	ADD.B	ROL,@_c_add:3	32
00000024	INC.W	#1,E0	
00000026	CMP.W	#2:3,E0	
00000028	BLO	L10:8	
0000002A	INC.W	#1,R1	
0000002C	CMP.W	#7:3,R1	
0000002E	BLO	L8:8	
00000030	MOV.W	E1,@_s_val:32	2
00000036	POP.W	R3	
0000038	RTS		
В			; section
00000000	_s_val:		; static: s_val
00000000	.RES.W	1	
00000002	_c_add:		; static: c_add
00000002	.RES.B	1	_

Table 1 lists the results of compilation with the H8S series, while Table 2 lists the result with the H8SX series.

Table 1 Results of Compilation (H8S Series)

		Instruction length Execution		Execution sta	state count	
H8S series		In bytes	Total	State count	Total	
CMP.W #	2,E0	4	4	2	2	

Table 2 Results of Compilation (H8SX Series)

	Instruction length		Execution state count	
H8SX series	In bytes	Total	State count	Total
CMP.W #2:3,E0	1	1	1	1



Revision Record

		Descripti	ion	
Rev.	Date	Page	Summary	
1.00	Sept.19.03		First edition issued	



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