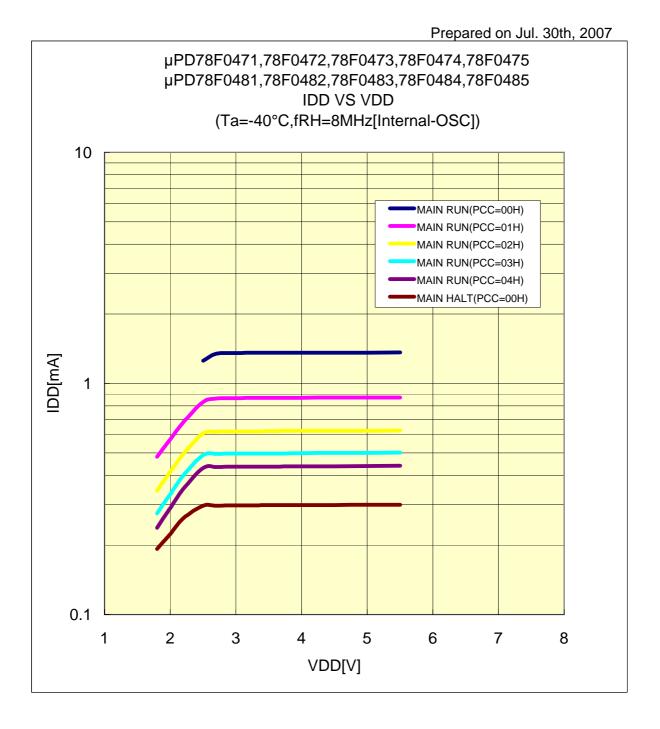
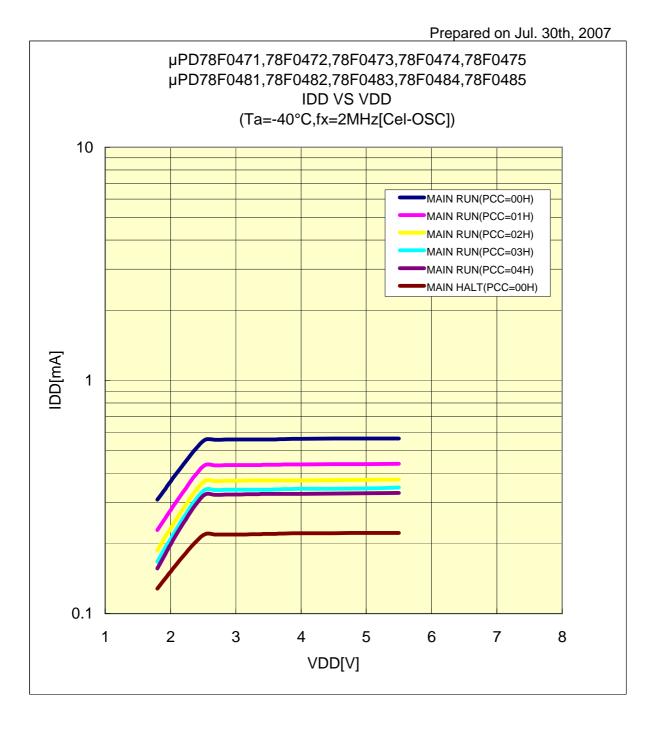
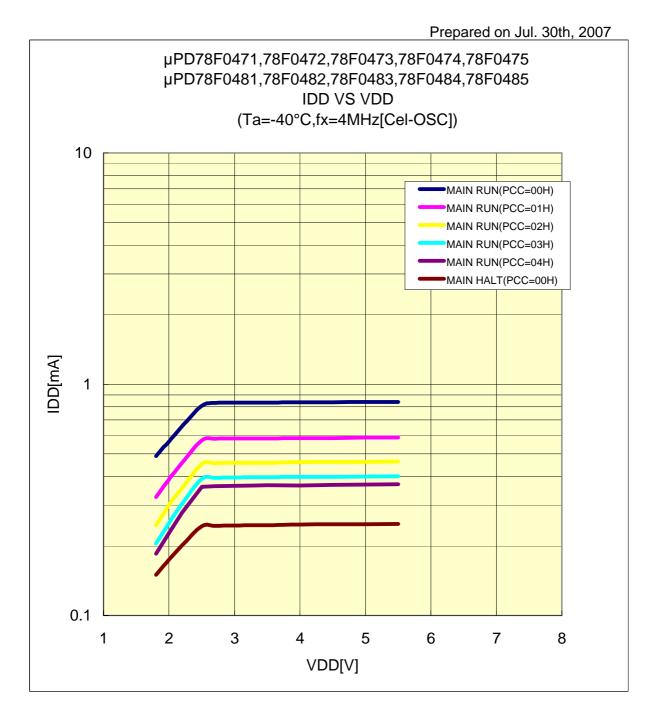
# IDD VS VDD(-40°C/8MHz[Internal-OSC])



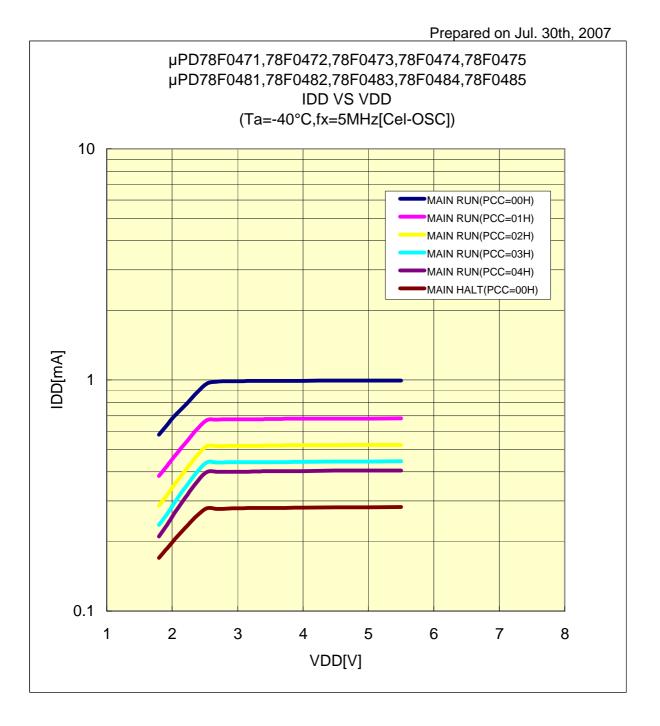
# IDD VS VDD(-40°C/2MHz[Cel-OSC])



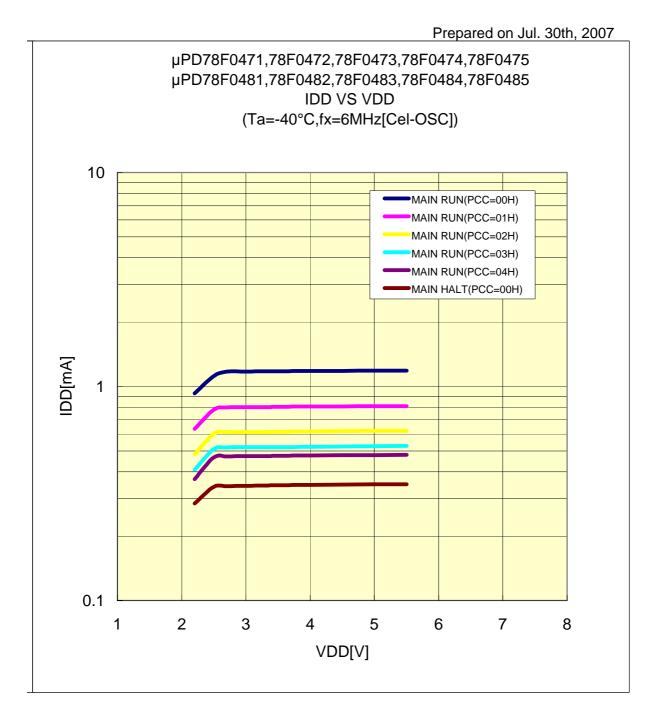
# IDD VS VDD(-40°C/4MHz[Cel-OSC])



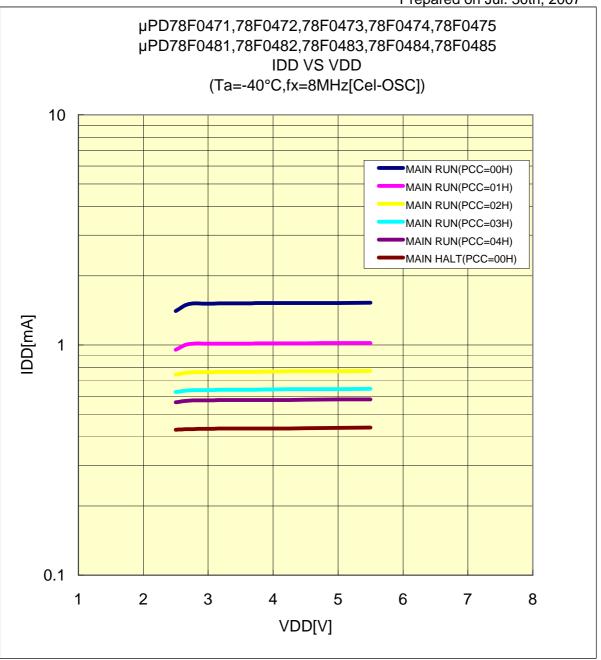
# IDD VS VDD(-40°C/5MHz[Cel-OSC])



# IDD VS VDD(-40°C/6MHz[Cel-OSC])

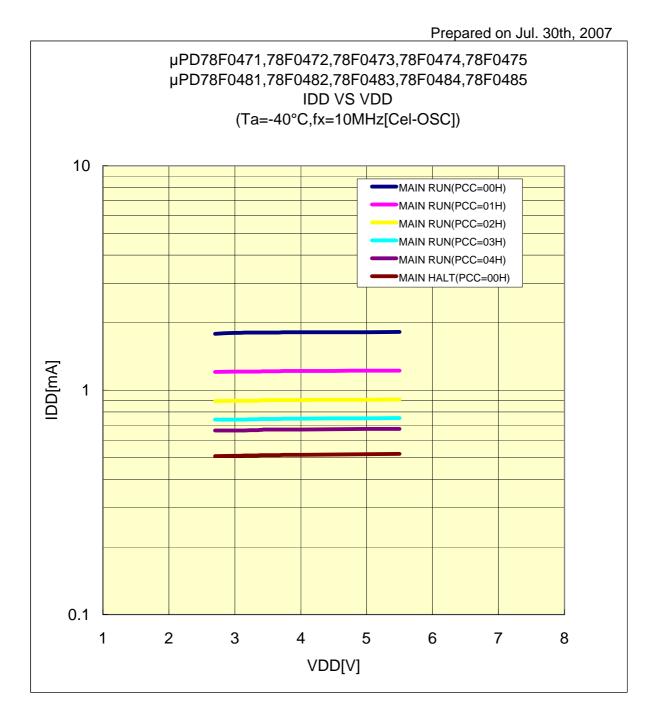


# IDD VS VDD(-40°C/8MHz[Cel-OSC])

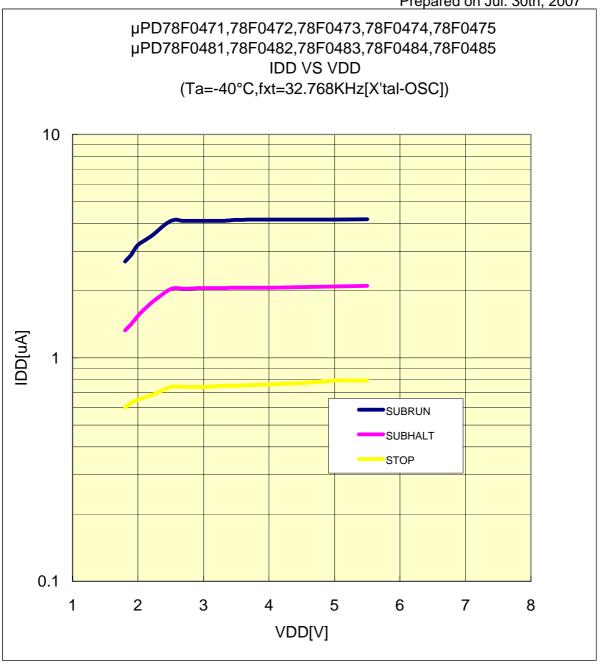


The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

# IDD VS VDD(-40°C/10MHz[Cel-OSC])



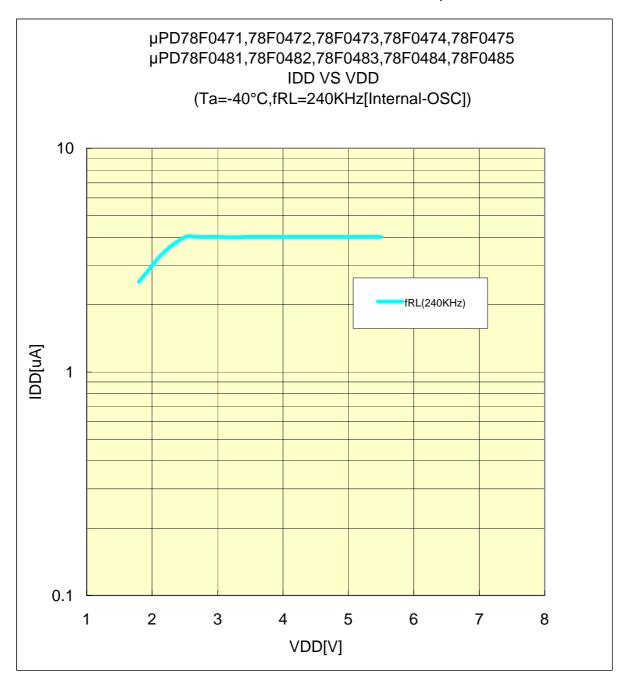
### IDD VS VDD(-40°C/32.768KHz[X'tal-OSC])



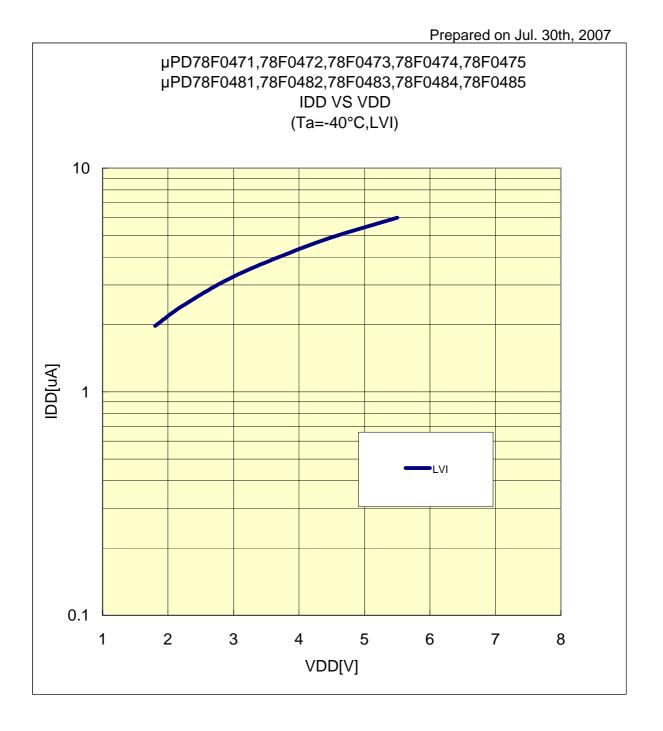
The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

### IDD VS VDD(-40°C/240KHz[Internal-OSC])

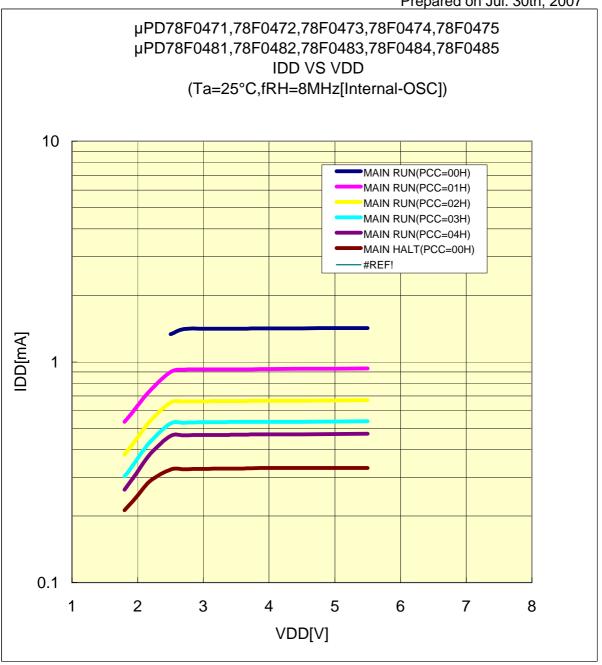
Prepared on Jul. 30th, 2007



### IDD VS VDD(-40°C/LVI)

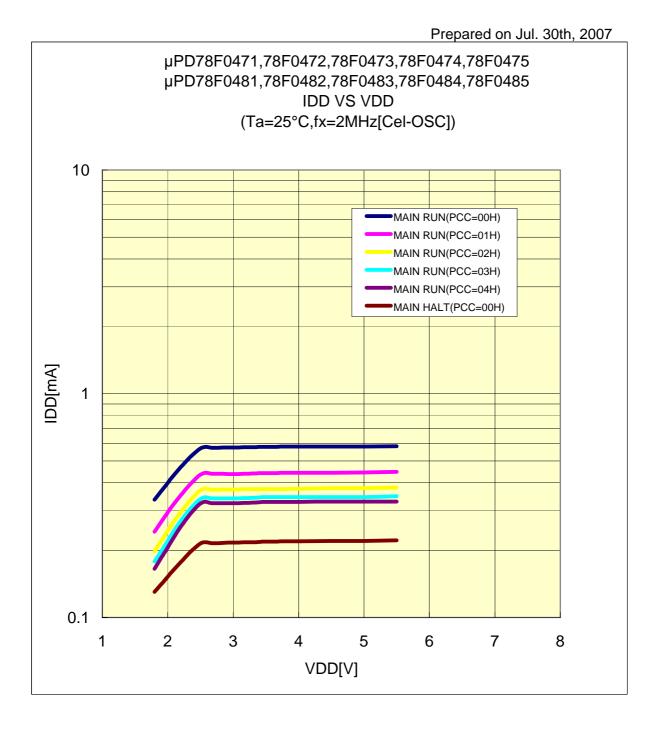


# IDD VS VDD(25°C/8MHz[Internal-OSC])

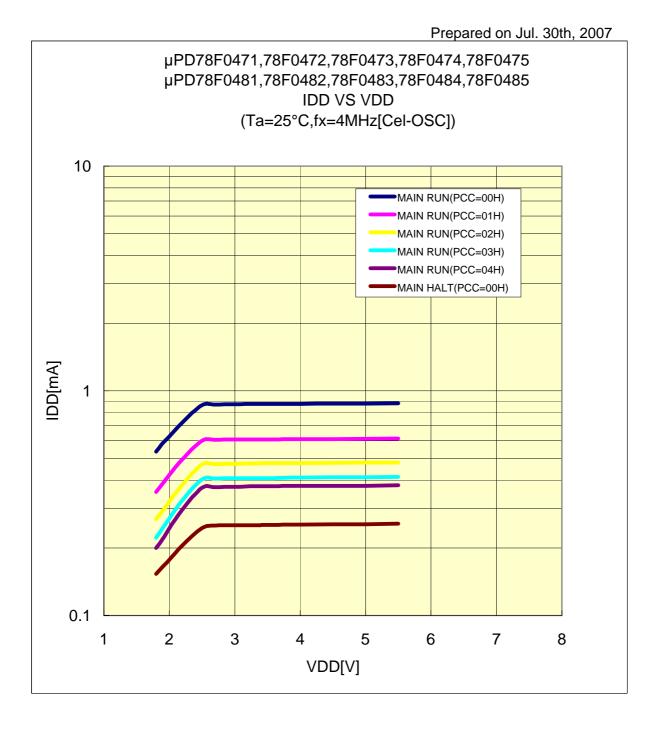


The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

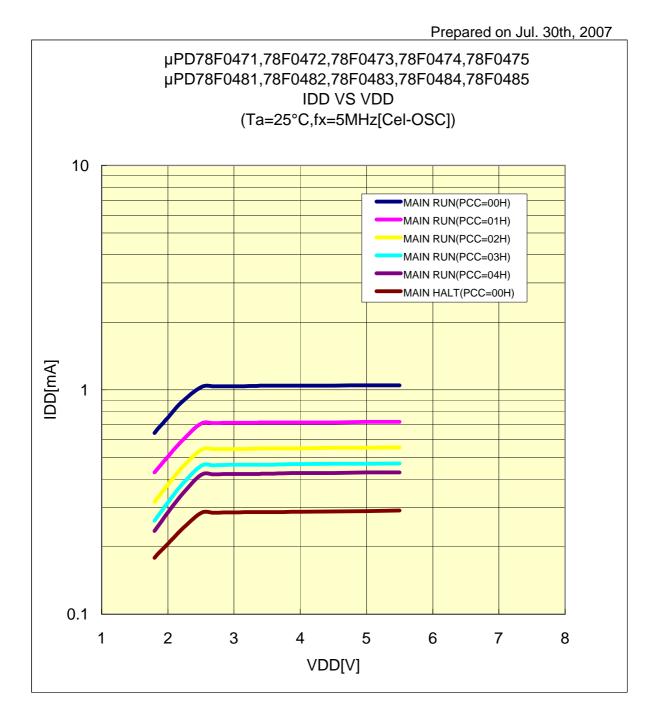
# IDD VS VDD(25°C/2MHz[Cel-OSC])



# IDD VS VDD(25°C/4MHz[Cel-OSC])



# IDD VS VDD(25°C/5MHz[Cel-OSC])



# IDD VS VDD(25°C/6MHz[Cel-OSC])

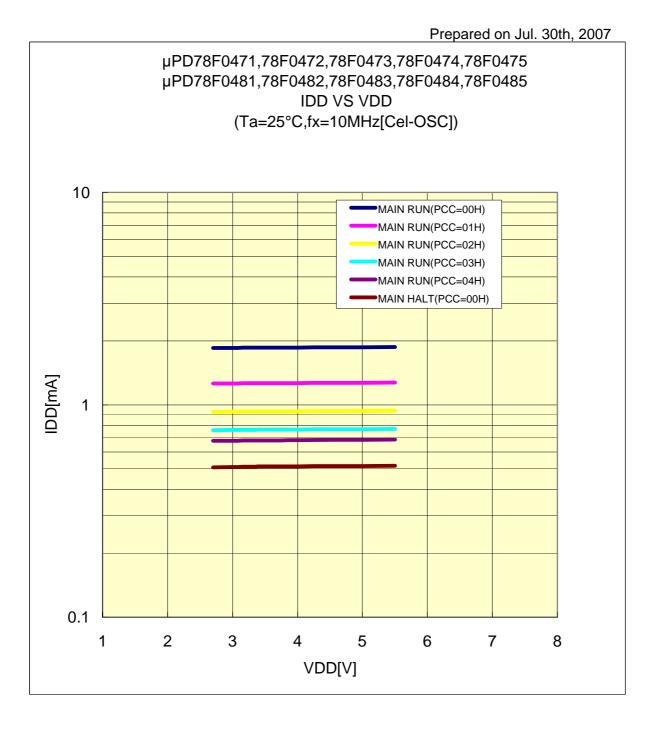
µPD78F0471,78F0472,78F0473,78F0474,78F0475 µPD78F0481,78F0482,78F0483,78F0484,78F0485 IDD VS VDD (Ta=25°C,fx=6MHz[Cel-OSC]) 10 MAIN RUN(PCC=00H) MAIN RUN(PCC=01H) MAIN RUN(PCC=02H) MAIN RUN(PCC=03H) MAIN RUN(PCC=04H) MAIN HALT(PCC=00H) [DD[mA] 1 0.1 1 2 3 4 5 6 7 8 VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

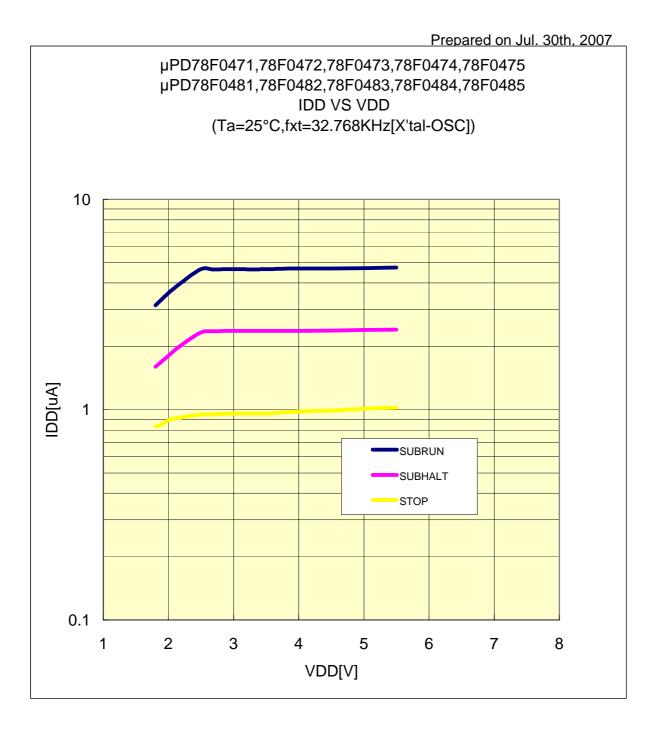
# IDD VS VDD(25°C/8MHz[Cel-OSC])

Prepared on Jul. 30th, 2007 µPD78F0471,78F0472,78F0473,78F0474,78F0475 µPD78F0481,78F0482,78F0483,78F0484,78F0485 IDD VS VDD (Ta=25°C,fx=8MHz[Cel-OSC]) 10 MAIN RUN(PCC=00H) MAIN RUN(PCC=01H) MAIN RUN(PCC=02H) MAIN RUN(PCC=03H) MAIN RUN(PCC=04H) MAIN HALT(PCC=00H) IDD[mA] 1 0.1 2 3 6 8 1 4 5 7 VDD[V]

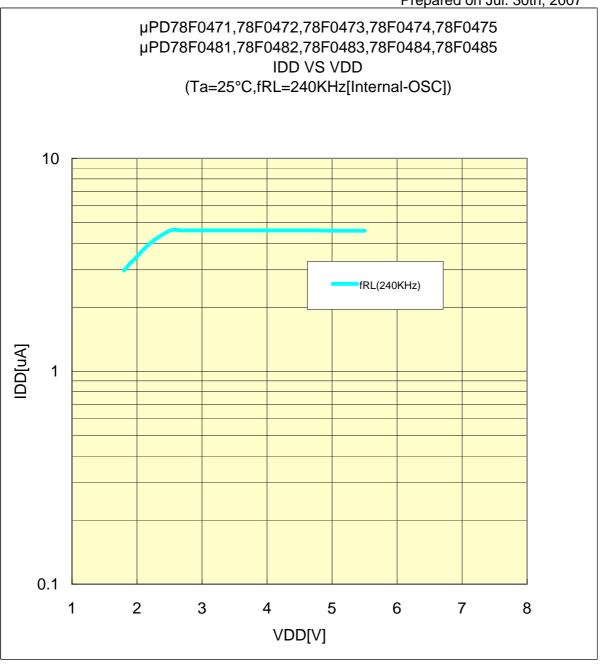
# IDD VS VDD(25°C/10MHz[Cel-OSC])



### IDD VS VDD(25°C/32.768KHz[Cel-OSC])

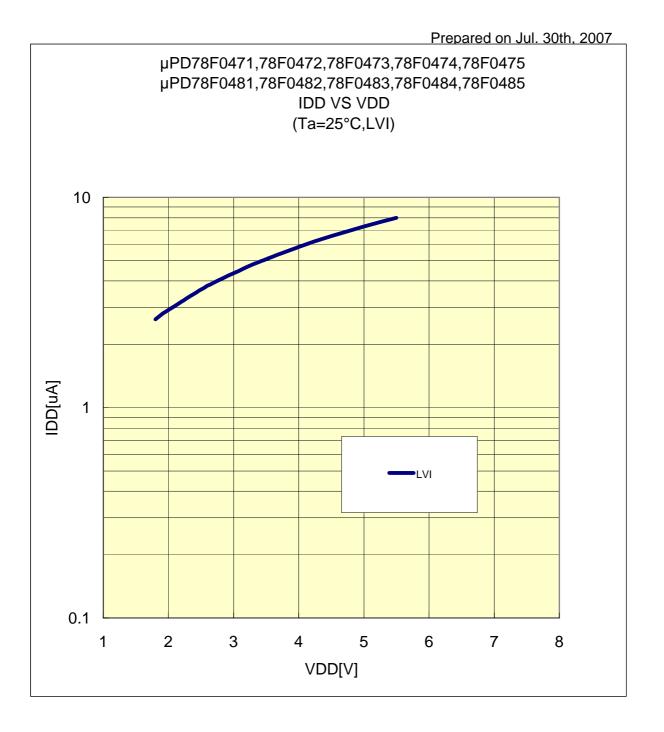


### IDD VS VDD(25°C/240KHz[Internal-OSC])

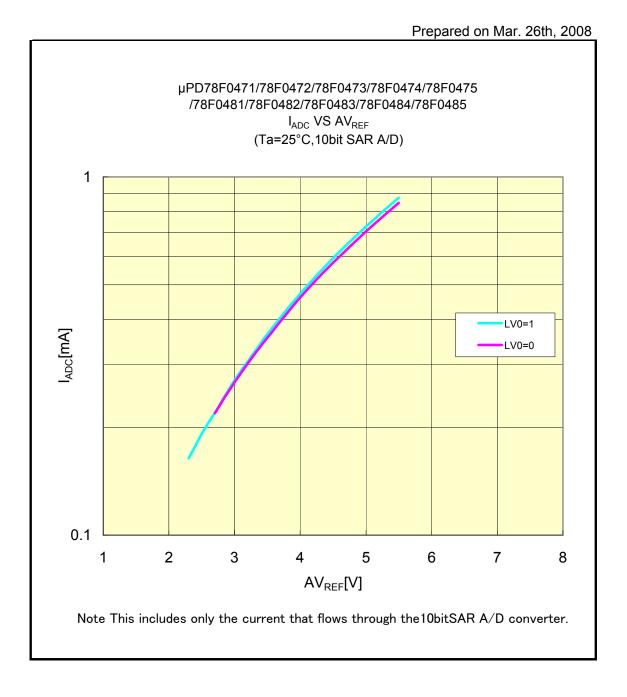


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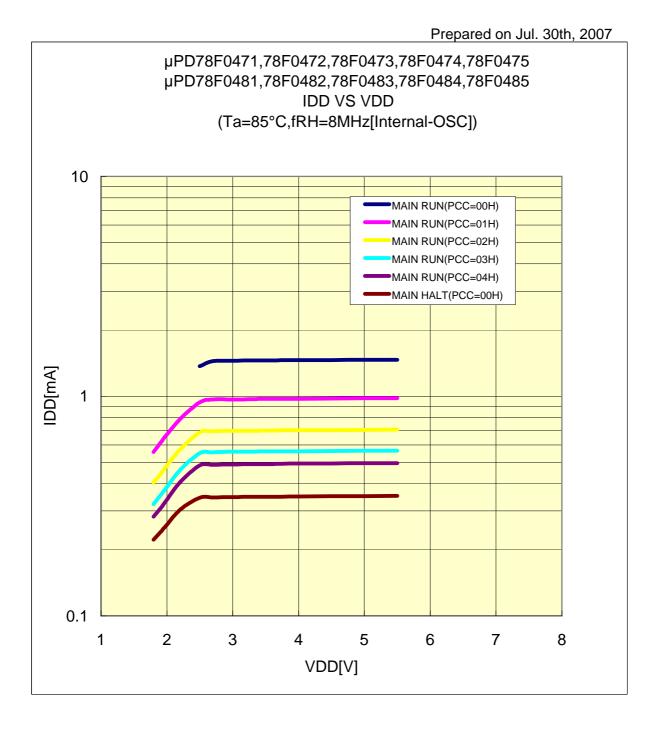
### IDD VS VDD(25°C/LVI)



# I<sub>ADC</sub> VS AV<sub>REF</sub>(25°C/10bit SAR A/D)



# IDD VS VDD(85°C/8MHz[Internal-OSC])

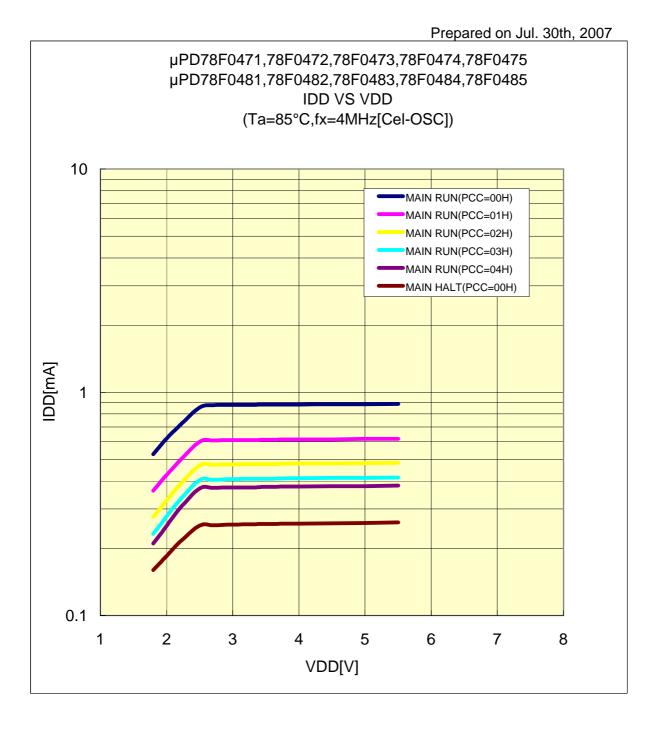


# IDD VS VDD(85°C/2MHz[Cel-OSC])

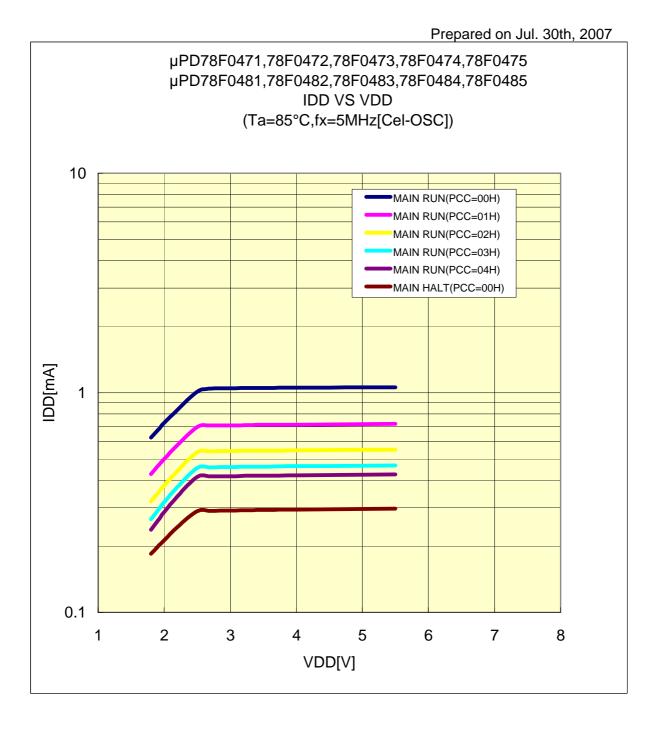
µPD78F0471,78F0472,78F0473,78F0474,78F0475 µPD78F0481,78F0482,78F0483,78F0484,78F0485 IDD VS VDD (Ta=85°C,fx=2MHz[Cel-OSC]) 10 MAIN RUN(PCC=00H) MAIN RUN(PCC=01H) MAIN RUN(PCC=02H) MAIN RUN(PCC=03H) MAIN RUN(PCC=04H) MAIN HALT(PCC=00H) IDD[mA] 1 0.1 1 2 3 5 7 8 4 6 VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

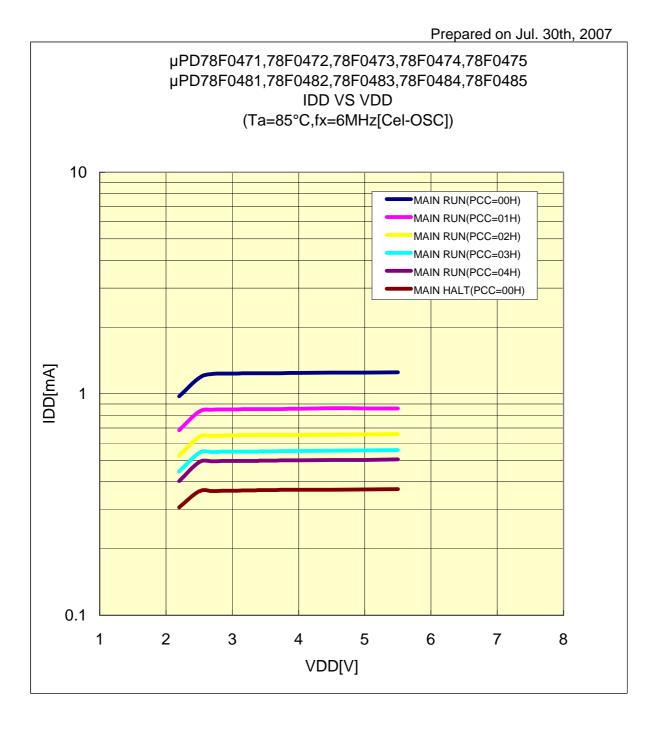
# IDD VS VDD(85°C/4MHz[Cel-OSC])



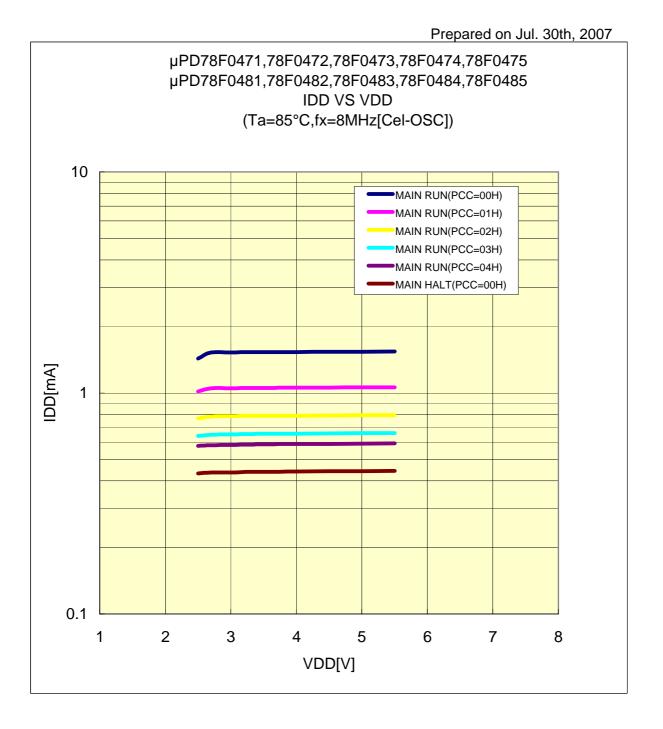
# IDD VS VDD(85°C/5MHz[Cel-OSC])



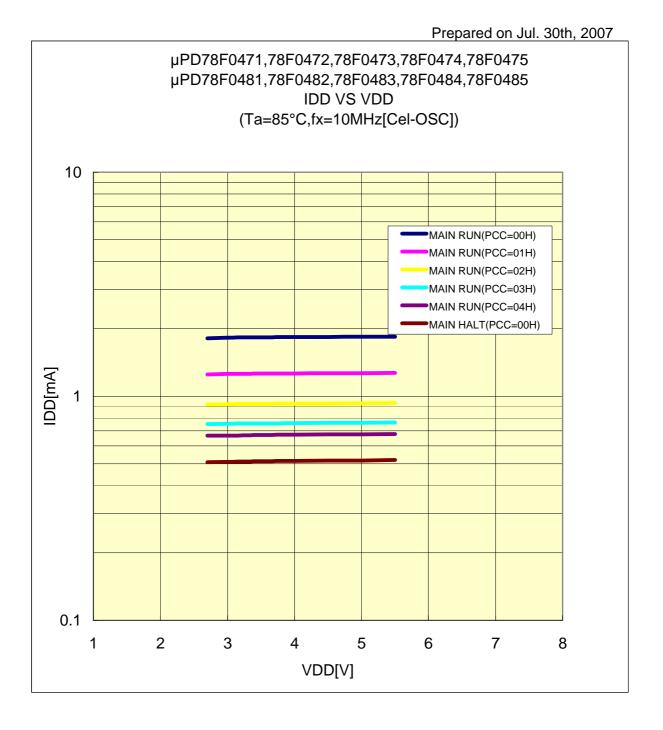
# IDD VS VDD(85°C/6MHz[Cel-OSC])



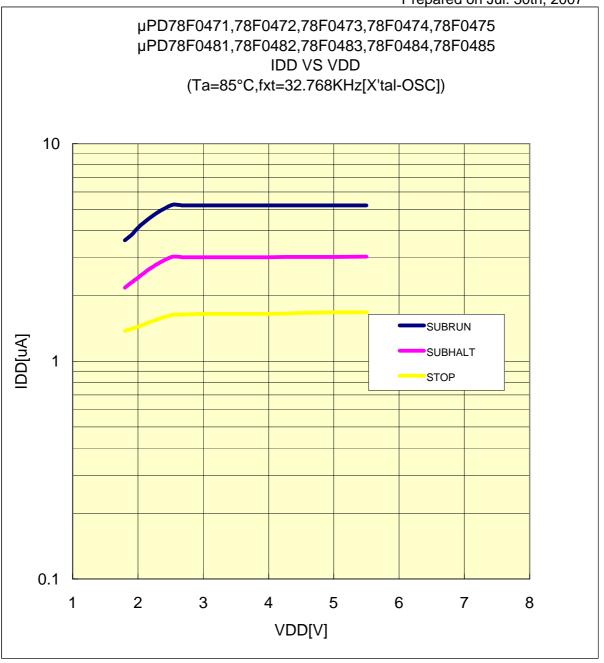
# IDD VS VDD(85°C/8MHz[Cel-OSC])



# IDD VS VDD(85°C/10MHz[Cel-OSC])

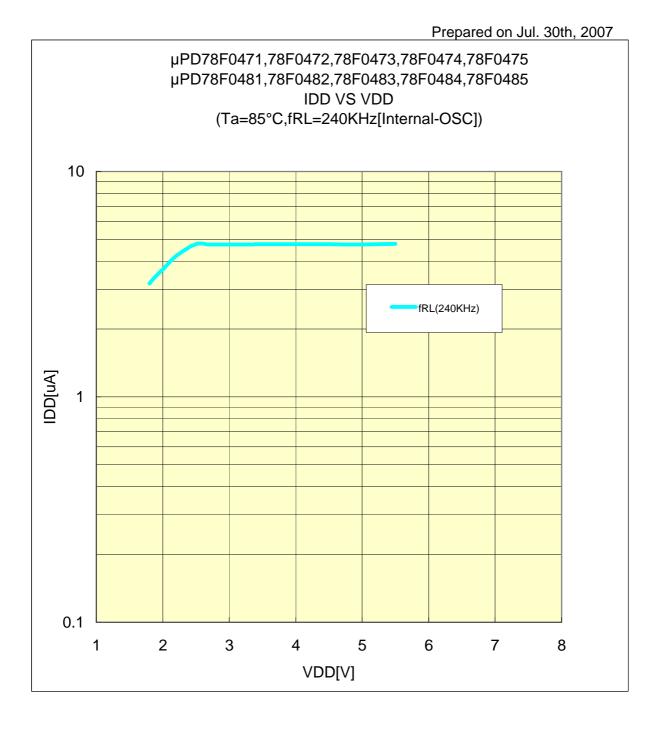


### IDD VS VDD(85°C/32.768KHz[X'tal-OSC])

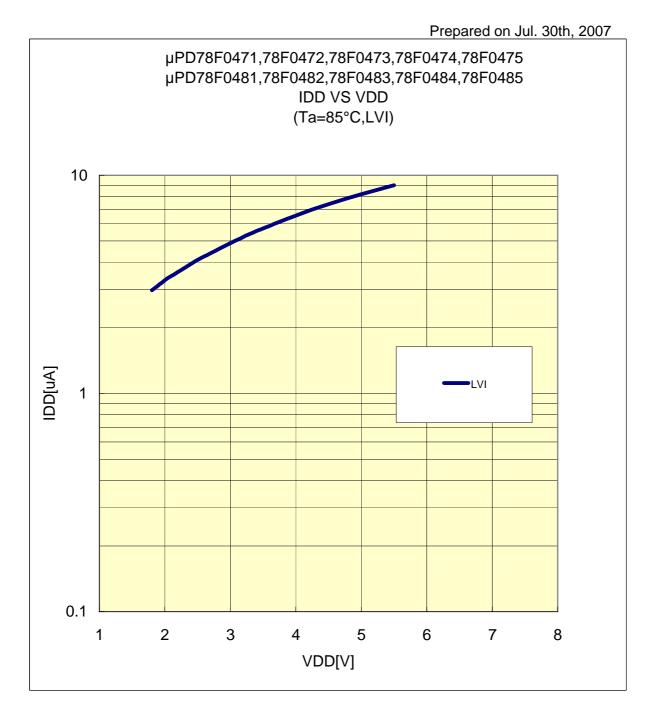


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## IDD VS VDD(85°C/240KHz[Internal-OSC])



# IDD VS VDD(85°C/LVI)



# I<sub>ADC</sub> VS AV<sub>REF</sub>(25°C/10bit SAR A/D)

