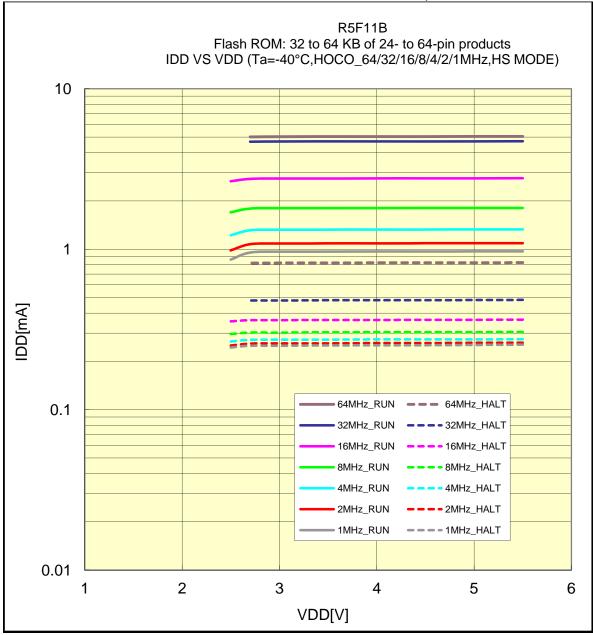
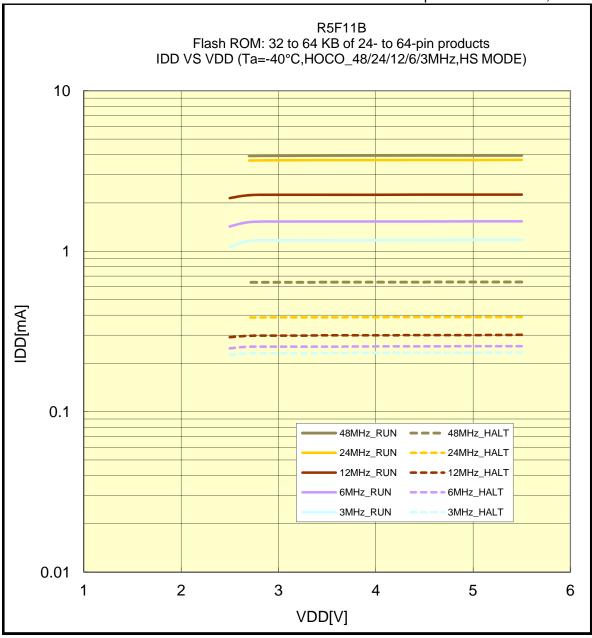
IDD VS VDD(-40°C/HOCO_64/32/16/8/4/2/1MHz/HS MODE)



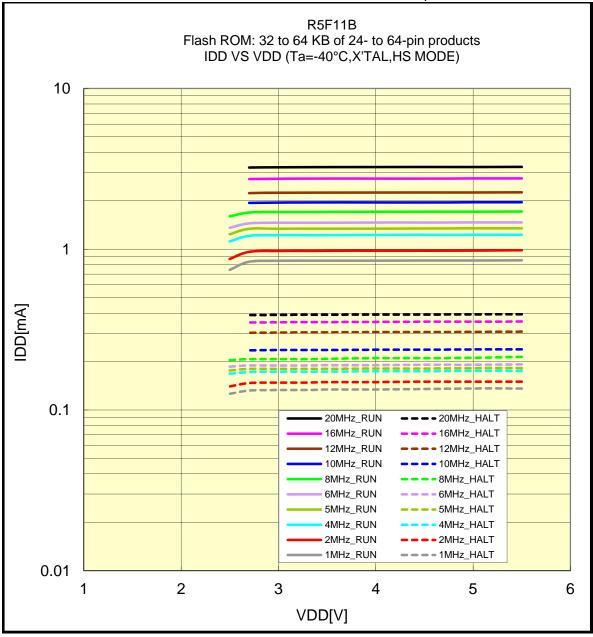
Prepared on Jan. 13th, 2015

IDD VS VDD(-40°C/HOCO_48/24/12/6/3MHz/HS MODE)



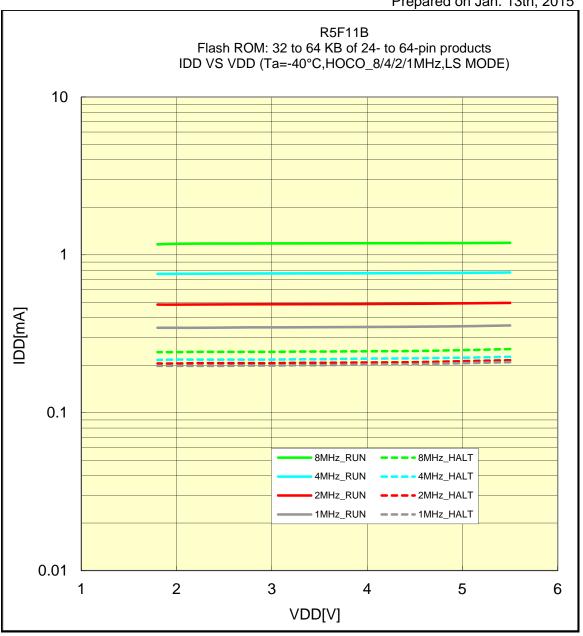
Prepared on Jan. 13th, 2015

IDD VS VDD(-40°C/X'TAL/HS MODE)



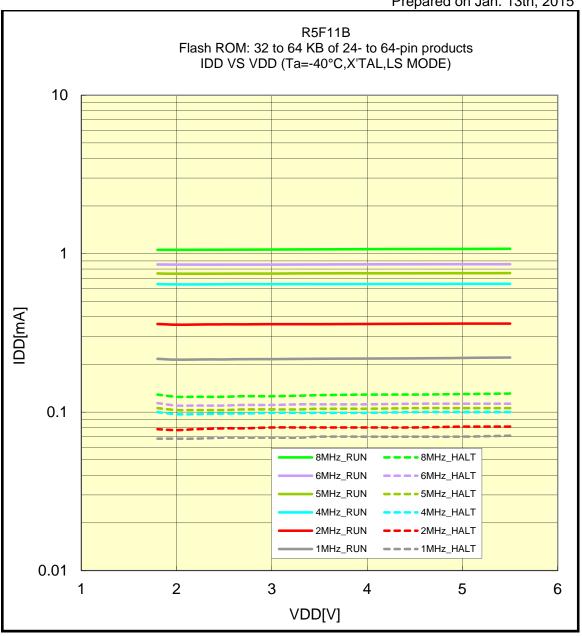
Prepared on Jan. 13th, 2015

IDD VS VDD(-40°C/HOCO_8/4/2/1MHz/LS MODE)



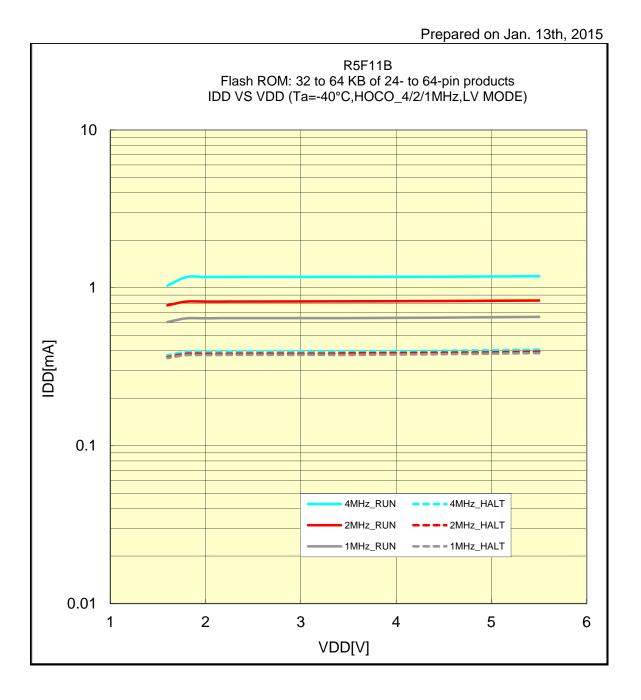
Prepared on Jan. 13th, 2015

IDD VS VDD(-40°C/X'TAL/LS MODE)

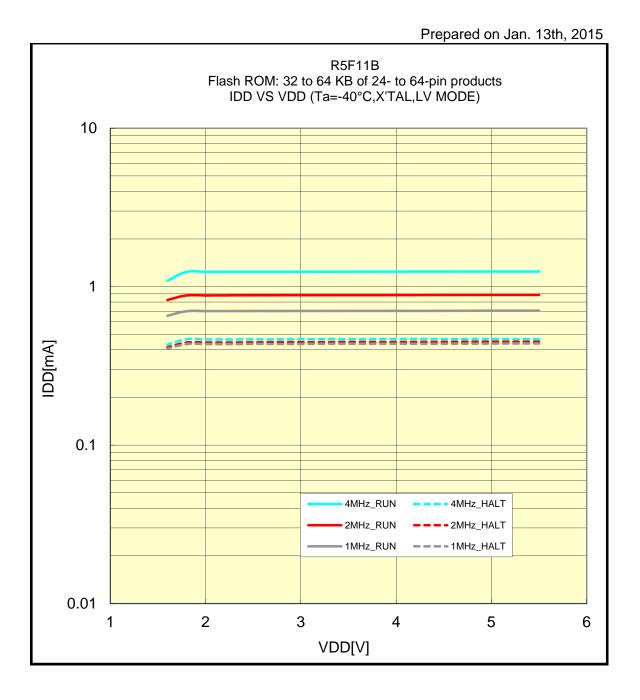


Prepared on Jan. 13th, 2015

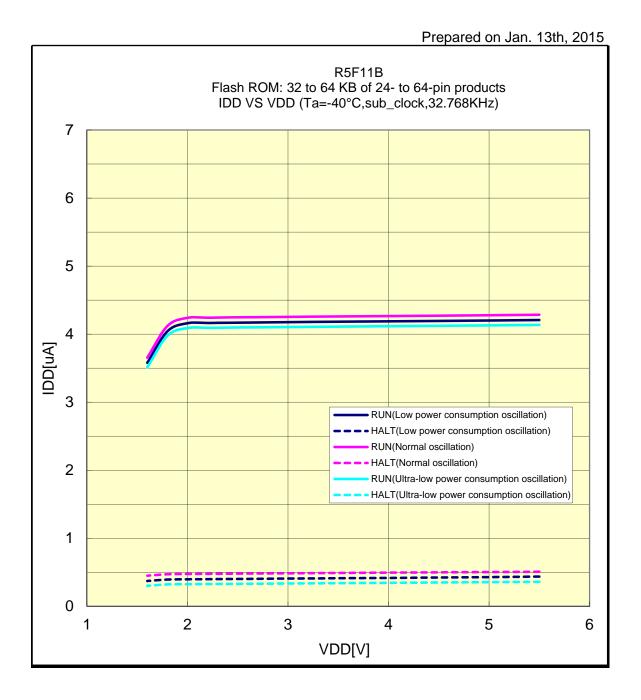
IDD VS VDD(-40°C/HOCO_4/2/1MHz/LV MODE)



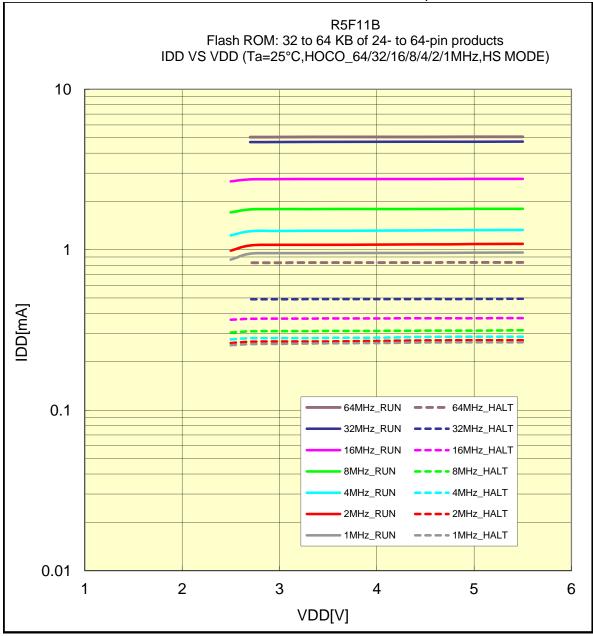
IDD VS VDD(-40°C/X'TAL/LV MODE)



IDD VS VDD(-40°C/sub_clock/32.768KHz)

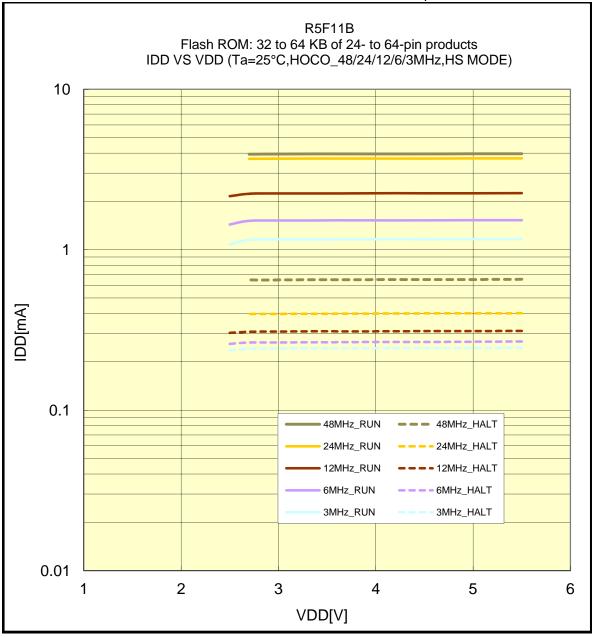


IDD VS VDD(25°C/HOCO_64/32/16/8/4/2/1MHz/HS MODE)



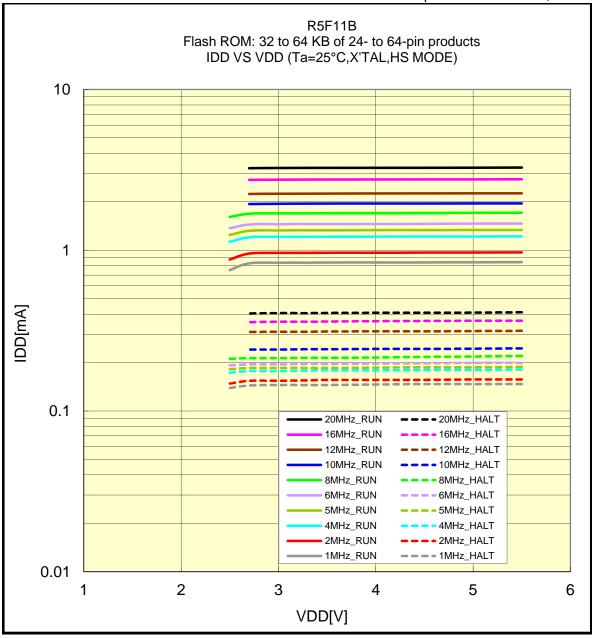
Prepared on Jan. 13th, 2015

IDD VS VDD(25°C/HOCO_48/24/12/6/3MHz/HS MODE)



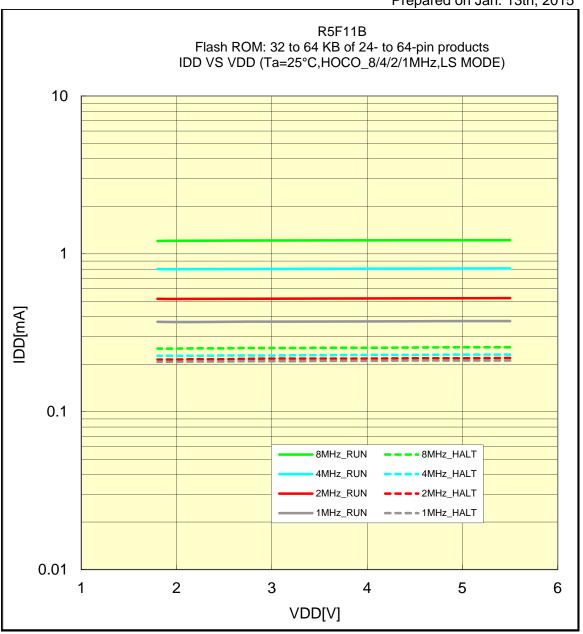
Prepared on Jan. 13th, 2015

IDD VS VDD(25°C/X'TAL/HS MODE)



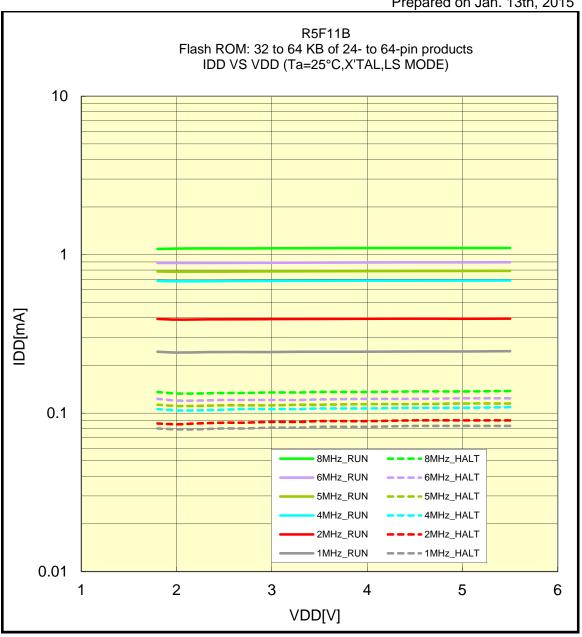
Prepared on Jan. 13th, 2015

IDD VS VDD(25°C/HOCO_8/4/2/1MHz/LS MODE)



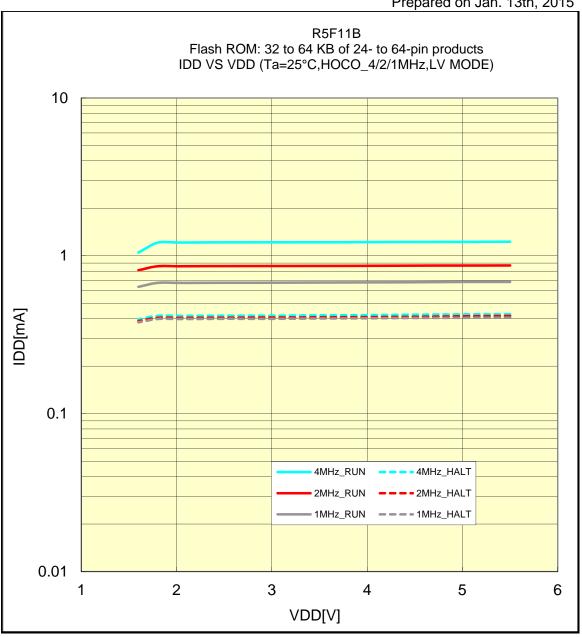
Prepared on Jan. 13th, 2015

IDD VS VDD(25°C/X'TAL/LS MODE)



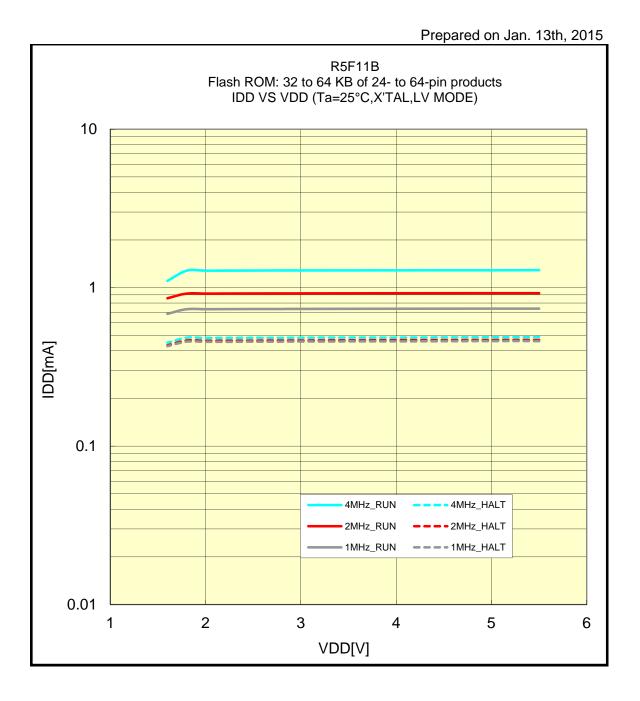
Prepared on Jan. 13th, 2015

IDD VS VDD(25°C/HOCO_4/2/1MHz/LV MODE)

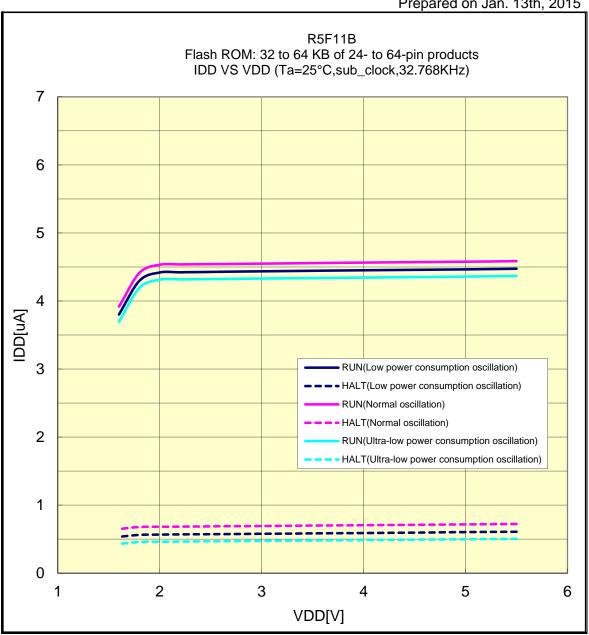


Prepared on Jan. 13th, 2015

IDD VS VDD(25°C/X'TAL/LV MODE)

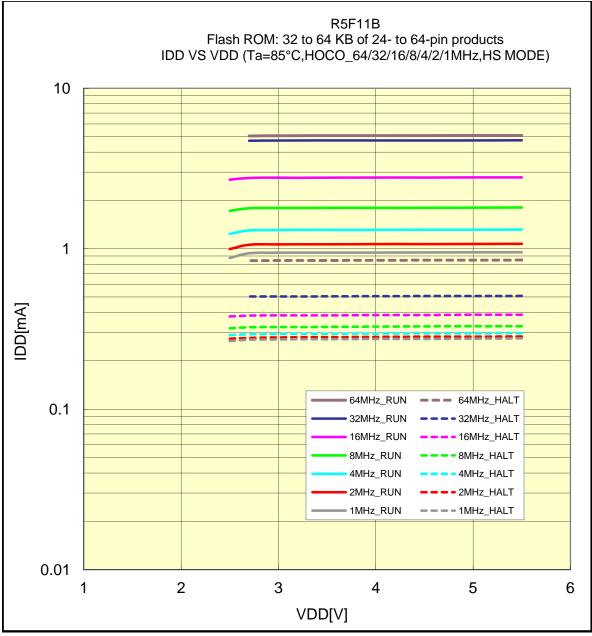


IDD VS VDD(25°C/sub_clock/32.768KHz)



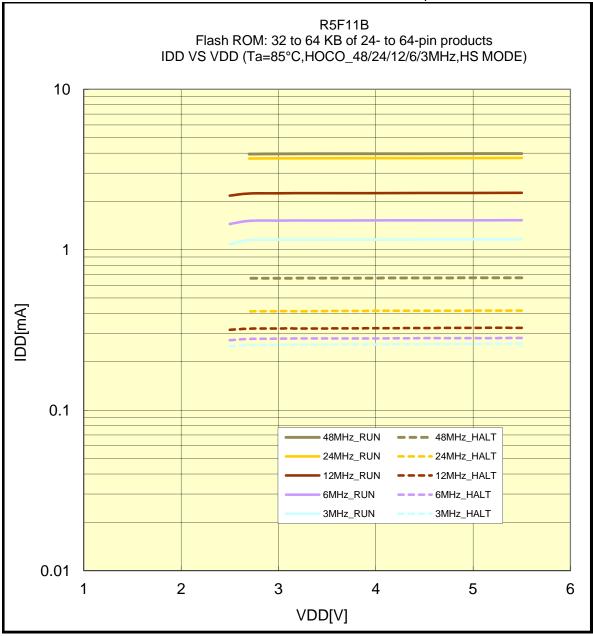
Prepared on Jan. 13th, 2015

IDD VS VDD(85°C/HOCO_64/32/16/8/4/2/1MHz/HS MODE)



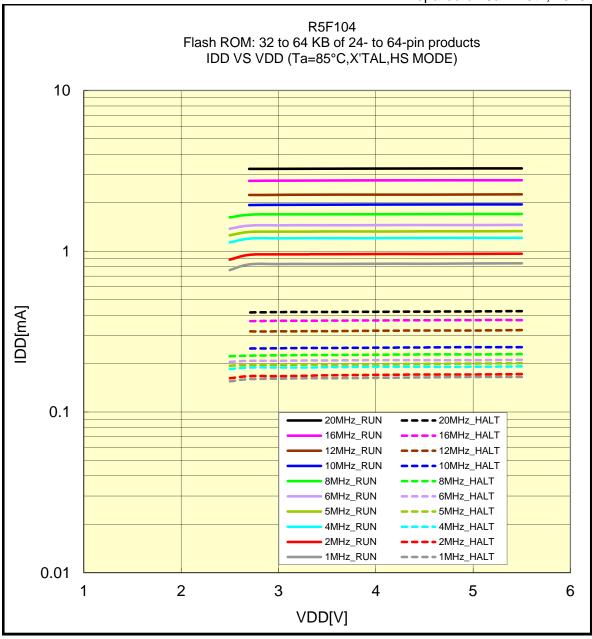
Prepared on Jan. 13th, 2015

IDD VS VDD(85°C/HOCO_48/24/12/6/3MHz/HS MODE)



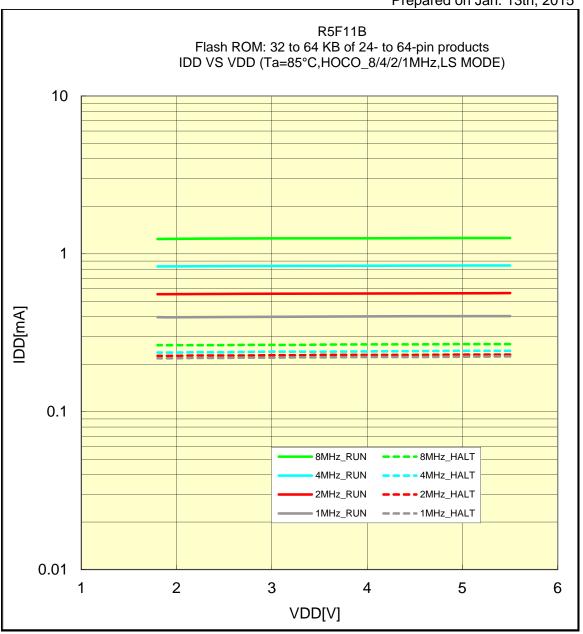
Prepared on Jan. 13th, 2015

IDD VS VDD(85°C/X'TAL/HS MODE)



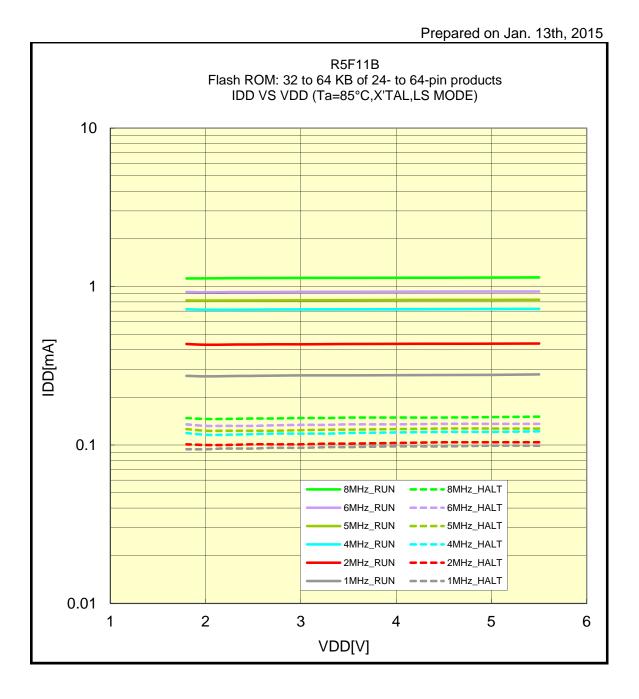
Prepared on Jan. 13th, 2015

IDD VS VDD(85°C/HOCO_8/4/2/1MHz/LS MODE)

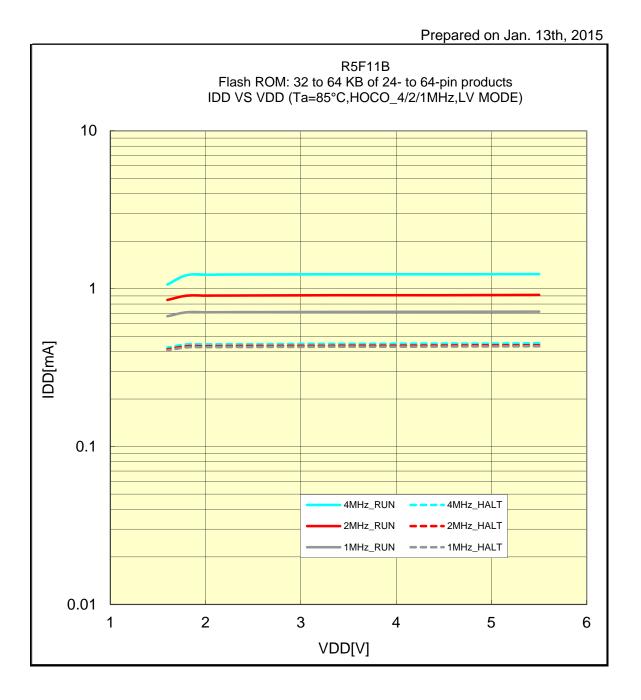


Prepared on Jan. 13th, 2015

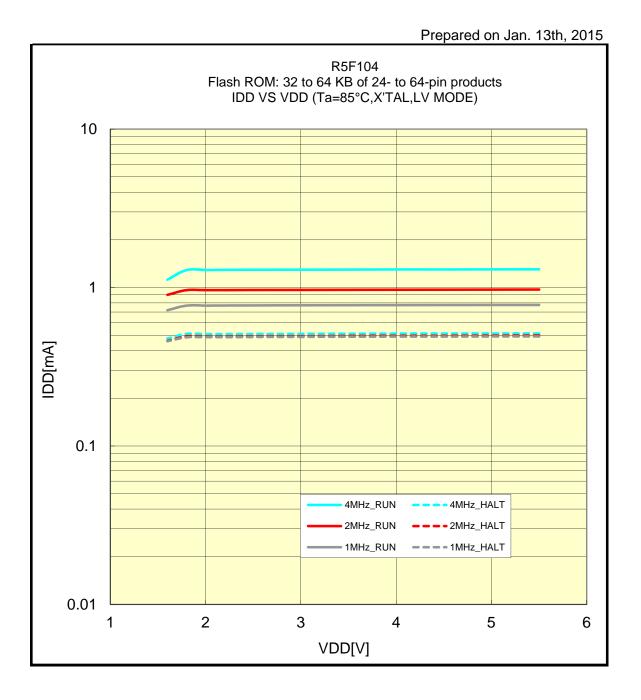
IDD VS VDD(85°C/X'TAL/LS MODE)



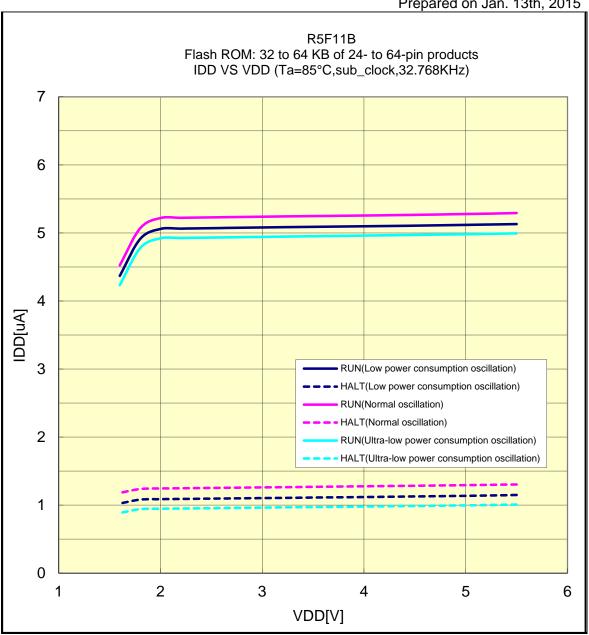
IDD VS VDD(85°C/HOCO_4/2/1MHz/LV MODE)



IDD VS VDD(85°C/X'TAL/LV MODE)

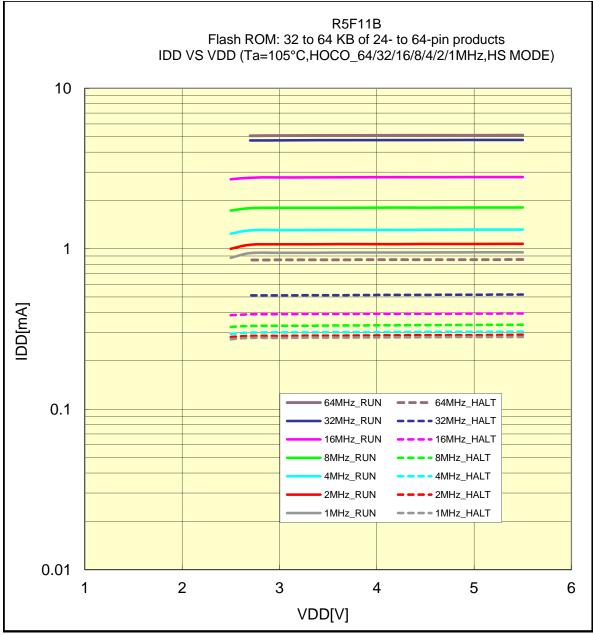


IDD VS VDD(85°C/sub_clock/32.768KHz)



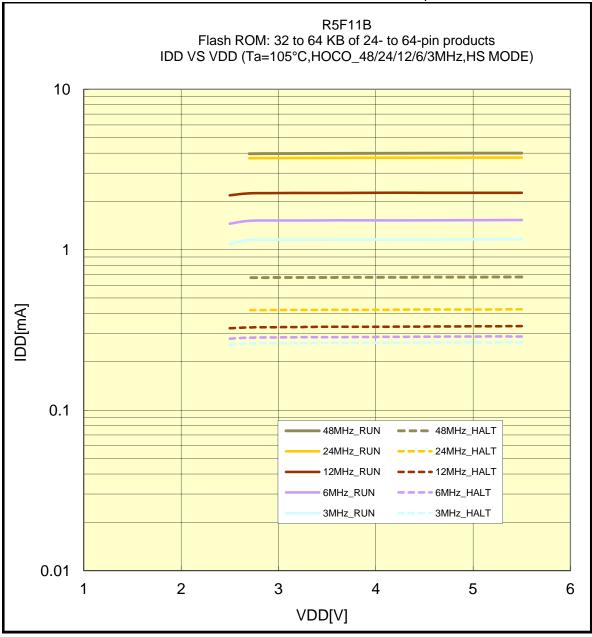
Prepared on Jan. 13th, 2015

IDD VS VDD(105°C/HOCO_64/32/16/8/4/2/1MHz/HS MODE)



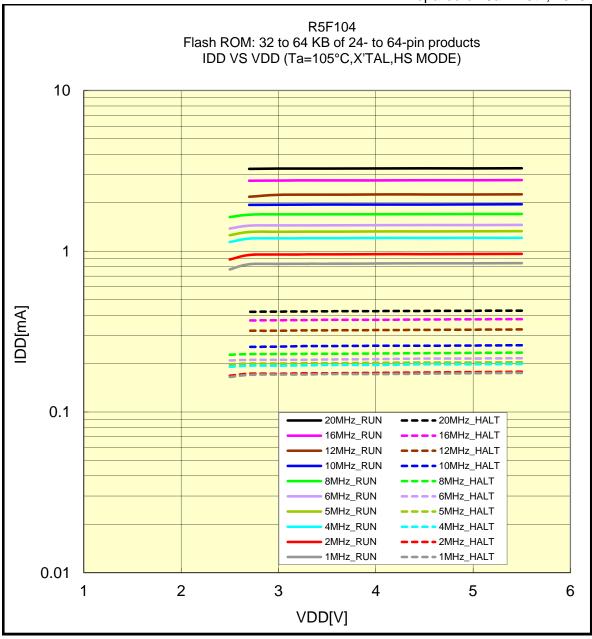
Prepared on Jan. 13th, 2015

IDD VS VDD(105°C/HOCO_48/24/12/6/3MHz/HS MODE)



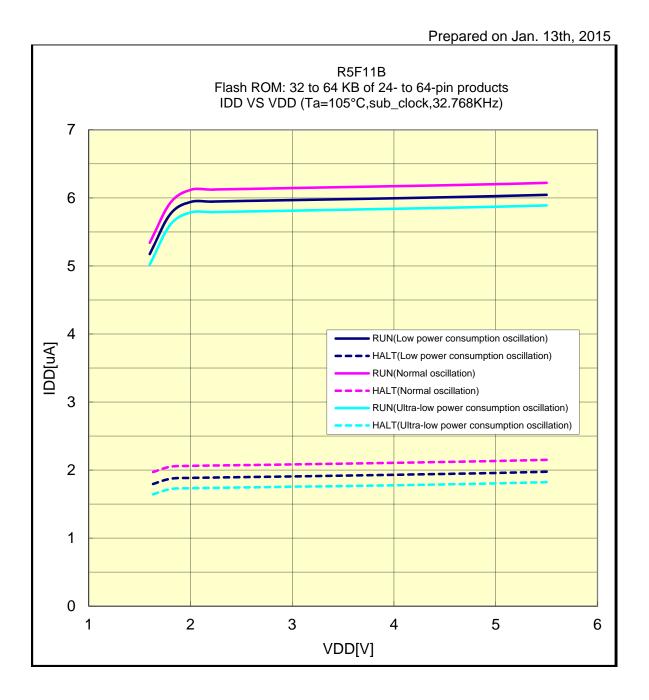
Prepared on Jan. 13th, 2015

IDD VS VDD(105°C/X'TAL/HS MODE)

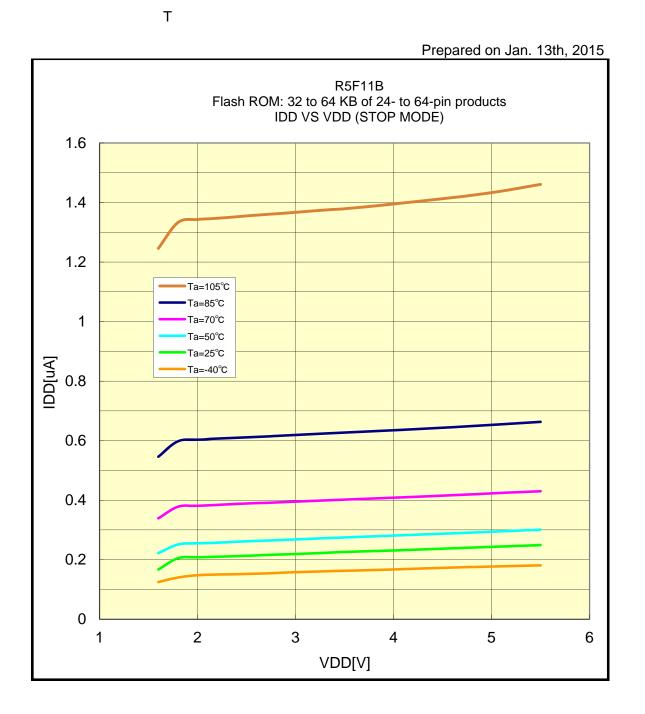


Prepared on Jan. 13th, 2015

IDD VS VDD(105°C/sub_clock/32.768KHz)



IDD VS VDD(STOP MODE)



IDD VS Ta(STOP MODE)

