

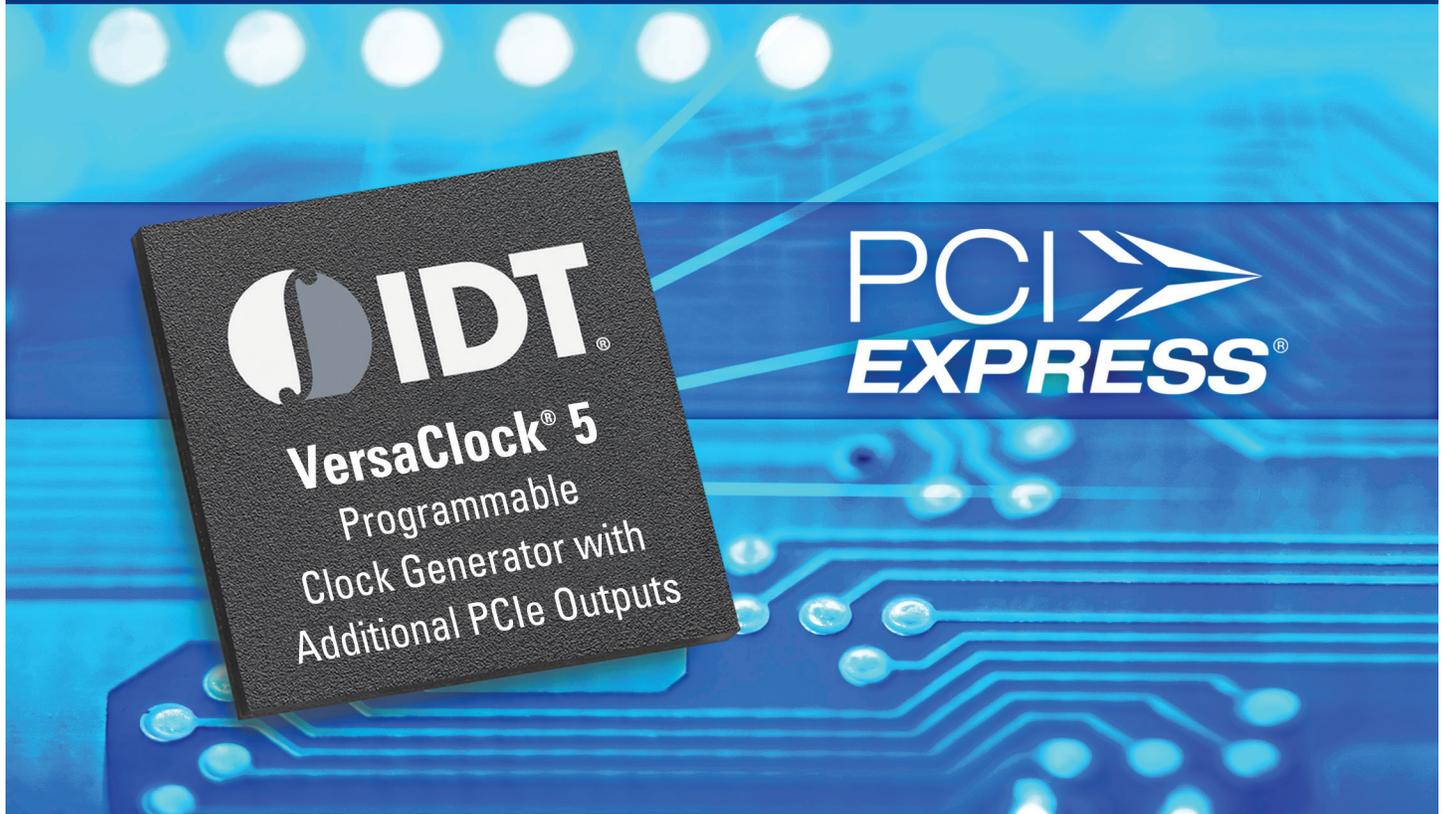


Integrated Device Technology

# 5P49V5907 and 5P49V5908

## VersaClock® 5 with Additional PCIe Outputs

CLOCKS AND TIMING | INTERFACE AND CONNECTIVITY | MEMORY AND LOGIC | POWER MANAGEMENT | RF PRODUCTS



### FEATURES:

- 3 programmable outputs with any frequency up to 350 MHz
- LVPECL, LVDS, HCSL, or Dual LVCMOS outputs
- Additional 4 / 8 copies of low-power HCSL (LP-HCSL) outputs to accommodate PCI Express timing needs
- High Performance
  - Less than 700 fsec RMS phase jitter
  - Meets the requirements of PCI Express Gen 1/2/3 as well as 1G / 10G Ethernet and other high performance protocols
- Low Power
  - 35 mA core current consumption
  - Low-power HCSL outputs for PCI Express clocks

### APPLICATIONS:

- High-end consumer
- Networking
- Computing
- Industrial
- Communications
- Broadcast video
- Medical

The VersaClock 5 5P49V5907 and 5P49V5908 devices have three outputs that can generate any frequency up to 350 MHz in various output types, and an additional four or eight LP-HCSL outputs specifically for PCI Express® clocking. The devices possess the same best-in-class jitter and power performance as the original 5P49V5901 device.



5 x 5 mm 40-VFQFPN package



6 x 6 mm 48-VFQFPN package

To request samples, download documentation, or learn more, visit: [idt.com/go/VersaClock5](http://idt.com/go/VersaClock5)

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