

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A919A/E	Rev.	1.00
Title	SH7450 Series User's Manual Hardware errata		Information Category	Technical information		
Applicable Product	SH7450 Series	Lot No.	Reference Document	Please refer to the related documents listed below.		
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1. Explanation

We inform you of the corrections of the following SH7450 Series User's Manuals.

[Related Documents]

Series	Group	Related Documents	Rev.	Doc Number
SH7450	SH7450,7451	User's Manual: Hardware	Rev.1.10	R01UH0286EJ0110
SH7450	SH7455,7456	User's Manual: Hardware	Rev.1.10	R01UH0030EJ0110
SH7450	SH7457,7459	User's Manual: Hardware	Rev.1.20	R01UH0420EJ0120

The changed/added description is written in red.

Chapter / Section number	Chapter / Section title	Item	Contents
23.4.3	Operation in Clock Synchronous Mode	Figure 23.13 Sample Flowchart for Transmitting Serial Data	<div style="display: flex; flex-direction: column;"> <div style="display: flex; align-items: center; margin-bottom: 10px;"> <div style="writing-mode: vertical-rl; transform: rotate(180deg); border: 1px solid black; padding: 2px 5px;">Error</div> <div style="flex-grow: 1;"> <pre> graph TD Start([Start of transmission]) --> ReadTDFE[Read TDFE flag in the SCiFSR register] ReadTDFE --> TDFE{ TDFE="1"? } TDFE -- No --> ReadTDFE TDFE -- YES --> WriteData[Write transmit data to the SCiFTDR register and clear TDFE flag in the SCiFSR register to "0"] WriteData --> AllData{ All data transmitted? } AllData -- No --> ReadTDFE AllData -- YES --> ReadTEND[Read TEND flag in the SCiFSR register] ReadTEND --> TEND{ TEND="1"? } TEND -- No --> ReadTDFE TEND -- YES --> ClearTEND[Clear TEND flag in the SCiFSR register to "0"] ClearTEND --> ClearTE[Clear TE bit in the SCiSCR register to "0"] ClearTE --> End([End of transmission]) </pre> </div> </div> <hr/> <div style="display: flex; align-items: center;"> <div style="writing-mode: vertical-rl; transform: rotate(180deg); border: 1px solid black; padding: 2px 5px;">Correction</div> <div style="flex-grow: 1;"> <pre> graph TD Start([Start of transmission]) --> ReadTDFE[Read TDFE flag in the SCiFSR register] ReadTDFE --> TDFE{ TDFE="1"? } TDFE -- No --> ReadTDFE TDFE -- YES --> WriteData[Write transmit data to SCiFTDR register, and then clear TDFE and TEND flags in SCiFSR register to "0".] WriteData --> AllData{ All data transmitted? } AllData -- No --> ReadTDFE AllData -- YES --> ReadTEND[Read TEND flag in the SCiFSR register] ReadTEND --> TEND{ TEND="1"? } TEND -- No --> ReadTDFE TEND -- YES --> ClearTEND[Clear TEND flag in the SCiFSR register to "0"] ClearTEND --> ClearTE[Clear TE bit in the SCiSCR register to "0"] ClearTE --> End([End of transmission]) </pre> </div> </div> </div>