

[Notes]

R20TS0690EJ0100

Rev.1.00

Apr. 16, 2021

On-chip Debugging Emulators

E2 Emulator, E2 Emulator Lite (when connected with RA)

Overview

When using the on-chip debugging emulator, E2 emulator or E2 emulator Lite, note the following point.

1. Using the E2 emulator or E2 emulator Lite with the RA family

1. Using the E2 Emulator or E2 Emulator Lite with the RA Family

1.1 Applicable Products

- E2 emulator, E2 emulator Lite

This applies when the above emulators are used with the following devices and integrated development environment (IDE).

- Applicable devices: RA4M2, RA4M3, RA6M4, RA6M5
- Applicable IDE: e² studio

1.2 Description and Conditions

The following phenomena may occur when you use the software break feature on code flash when the C cache is enabled or on SRAM (other than standby SRAM) when the S cache is enabled.

- The program does not stop at the designated software breakpoint.
- The program stops at the canceled software breakpoint.
- When you run a user program after it was stopped by a software break, it stops at the same address and does not proceed.

1.3 Workaround

Do not use the software break feature on code flash when the C cache is enabled or on SRAM (other than standby SRAM) when the S cache is enabled.

1.4 Permanent Measure

This problem will be fixed in the next version of e² studio. (Scheduled to be released in August 2021 or later.)

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr.16.21	-	First edition issued

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