

BCR16FM-12LC

600V - 16A - Triac

Medium Power Use

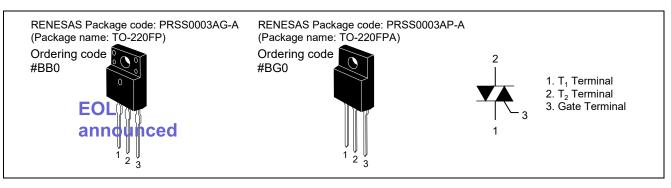
Features

- I_{T (RMS)} : 16 A
- V_{DRM} : 600 V
- Tj: 150°C •
- IFGTI, IRGTI, IRGT III: 50 mA

Insulated Type

- Planar Passivation Type
- Viso: 2000 V

Outline



Application

Low inrush current AC load.

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
		12	
Repetitive peak off-state voltage ^{Note1}	Vdrm	600	V
Non-repetitive peak off-state voltage ^{Note1}	VDSM	700	V

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	IT (RMS)	16	А	Commercial frequency, sine full wave
				360° conduction, Tc = 60° C
Surge on-state current	ITSM	96	Α	60 Hz sinewave 1 full cycle, peak value,
				non-repetitive
I ² t for fusion	l ² t	38	A ² s	Value corresponding to 1 cycle of half wave
				60 Hz, surge on-state current
Peak gate power dissipation	P _{GM}	5	W	
Average gate power dissipation	Pg (AV)	0.5	W	
Peak gate voltage	V_{GM}	10	V	
Peak gate current	lgм	2	А	
Junction Temperature	Tj	-40 to +150	°C	
Storage temperature	Tstg	-40 to +150	°C	
Isolation voltage Note6	Viso	2000	V	Ta=25°C, AC 1 minute,
				T ₁ • T ₂ • G terminal to case

Notes: 1. Gate open.

R07DS1412EJ0101

Data Sheet

Rev.1.01 Feb. 19, 2019

Electrical Characteristics

Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state cu	rrent	IDRM	_	_	2.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V _{TM}	—	—	1.75	V	Tc = 25°C, I _{TM} = 25 A, instantaneous measurement
Gate trigger voltage ^{Note2}	Ι	VFGTI			1.5	V	Tj = 25°C, V _D = 6 V, R _L = 6 Ω,
	II	V _{RGTI}	_	_	1.5	V	R _G = 330 Ω
	III	Vrgtiii	_	_	1.5	V	
]	Ι	IFGTI	_		50	mA	$\label{eq:constraint} \begin{array}{l} Tj = 25^\circC, V_D = 6 \; V, R_L = 6 \; \Omega, \\ R_G = 330 \; \Omega \end{array}$
	II	IRGTI	_		50	mA	
	III	IRGTIII	_	_	50	mA	
Gate non-trigger voltage		V _{GD}	0.2	_	_	V	Tj = 125°C, V _D = 1/2 V _{DRM}
Thermal resistance		Rth (j-c)	_	_	4.1	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-sta commutation voltage ^{Note4}	ate	(dv/dt)c	10	—	_	V/µs	Tj = 125°C

Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

3. The contact thermal resistance Rth(c-f) in case of greasing is 0.5°C /W.

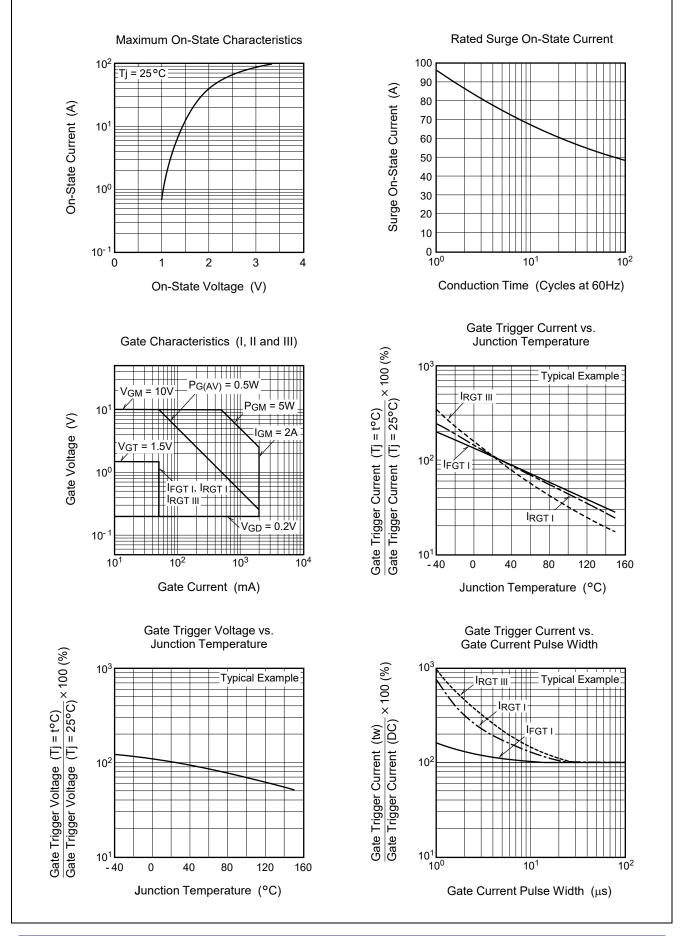
4. Test conditions of the critical-rate of rise of off-state commutation voltage is shown in the table below.

5. Make sure that your finished product containing this device meets your safe isolation requirements. For safety, it's advisable that heatsink is electrically floating.

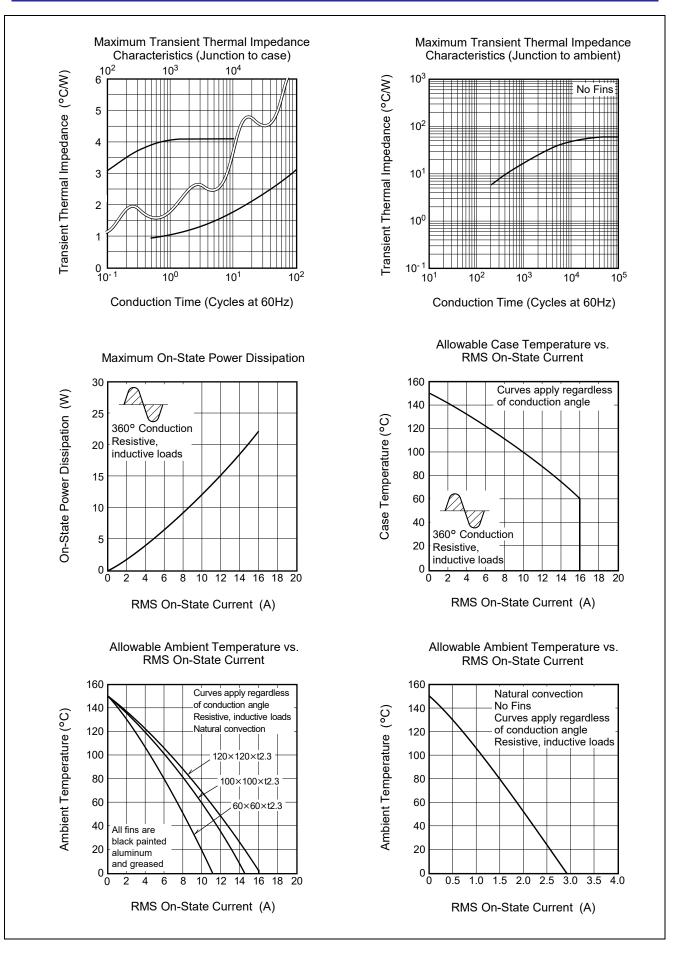
Test conditions	Commutating voltage and current waveforms (inductive load)		
 Junction temperature Tj = 125°C Rate of decay of on-state commutating current (di/dt)c = -8 A/ms Peak off-state voltage V_D = 400 V 	Supply Voltage Time Main Current (di/dt)c Main Voltage Time (dv/dt)c Time VD		



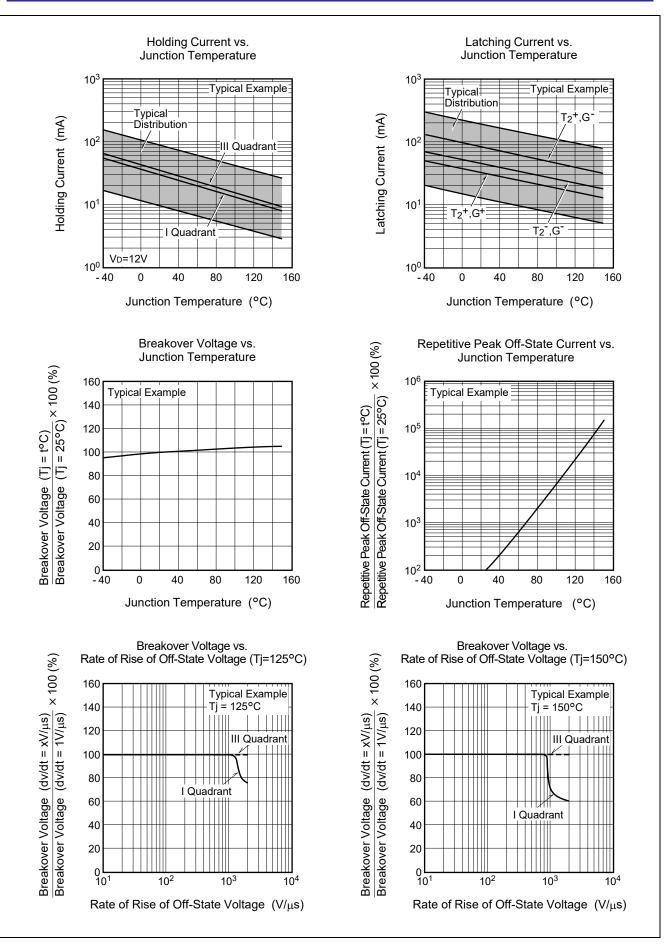
Performance Curves



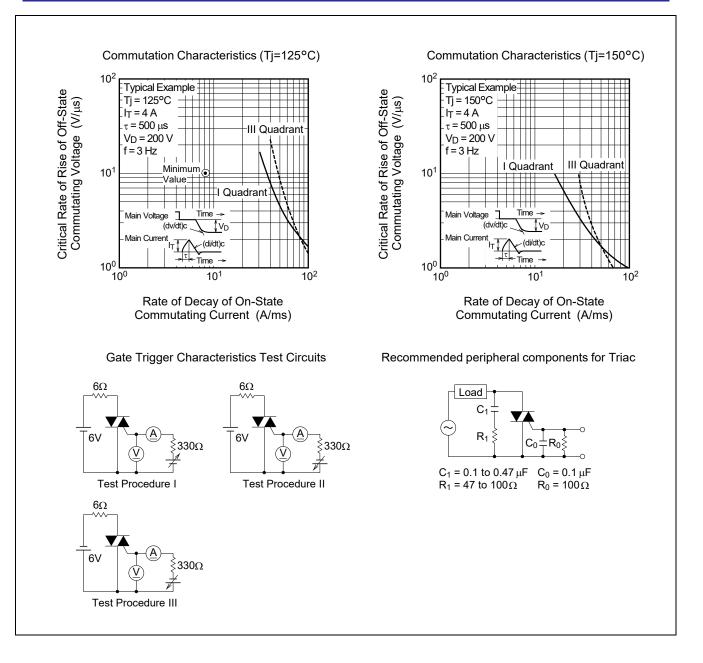




RENESAS



RENESAS



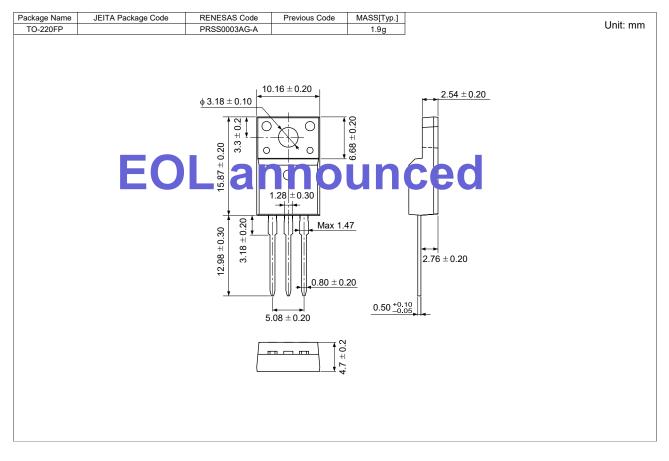
Package Dimensions

Ordering code: #BG0

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
- PRSS0003A		TO-220FPA	1.65
	2	.7±0.2	Unit: mm
	3.2±0.2		

Package Dimensions

Ordering code: #BB0 <EOL announced>



Ordering Information

Orderable Part Number	Package	Quantity Note6	Remark	Status
BCR16FM-12LC#BG0	TO-220FPA	50 pcs./ tube	Straight type	Mass Production
BCR16FM-12LCDD#BG0	TO-220FPA	50 pcs./ tube	□□:Lead form type	
BCR16FM-12LC#BB0	TO-220FP	50 pcs./ tube	Straight type	EOL announced
BCR16FM-12LCDD#BB0	TO-220FP	50 pcs./ tube	□□:Lead form type	

Notes: 6. Please confirm the specification about the shipping in detail.

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