

RJE0616JSP

-60V, -4A Silicon P Channel Thermal FET Power Switching

R07DS1234EJ0200 (Previous: REJ03G1944-0100)

> Rev.2.00 Oct 27, 2014

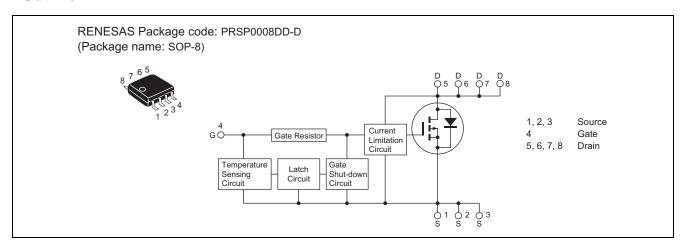
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- For Automotive applications
- Built-in the over temperature shut-down circuit.
- High endurance capability against to the short circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- Low on-resistance $R_{DS(on)}$: 77 m Ω Typ, 90 m Ω Max ($V_{GS} = -10 \text{ V}$)
- High density mounting
- AEC-Q101 compliant

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-60	V
Gate to source voltage	V_{GSS}	-16	V
_	V _{GSS}	2.5	V
Drain current	I _D Note3	-4	Α
Body-drain diode reverse drain current	I _{DR}	-4	Α
Avalanche current	I _{AP} Note 2	-4	Α
Avalanche energy	E _{AR} Note 2	68.6	mJ
Channel dissipation	Pch Note 1	2.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1 When using the glass epoxy board (FR4 $40 \times 40 \times 1.6$ mm), PW ≤ 10 s

- 2. Tch = 25°C, Rg \geq 50 Ω
- 3. It provides by the current limitation lower bound value.

Typical Operation Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	V _{IH}	-3.5	_	_	V	
	V_{IL}	_		-1.2	V	
Input current	I _{IH1}	_	_	-100	μΑ	$Vi = -8 V, V_{DS} = 0$
(Gate non shut down)	I _{IH2}	_	_	-50	μΑ	$Vi = -3.5 \text{ V}, V_{DS} = 0$
	I _{IL}	_	_	-1	μΑ	$Vi = -1.2 \text{ V}, V_{DS} = 0$
Input current	I _{IH(sd)1}	_	-0.8	_	mA	$Vi = -8 V, V_{DS} = 0$
(Gate shut down)	I _{IH(sd)2}	_	-0.35	_	mA	$Vi = -3.5 \text{ V}, V_{DS} = 0$
Shut down temperature	Tsd	_	175	_	°C	Channel temperature (dv/dt V _{GS} ≥ 500 V/ms)
Gate operation voltage	Vop	-3.5	_	-12	V	(474: 163 = 555 77115)
Drain current (Current limitation value)	I _{D limt}	-4	_	_	А	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$

Notes; 4. Pulse test

Electrical Characteristics

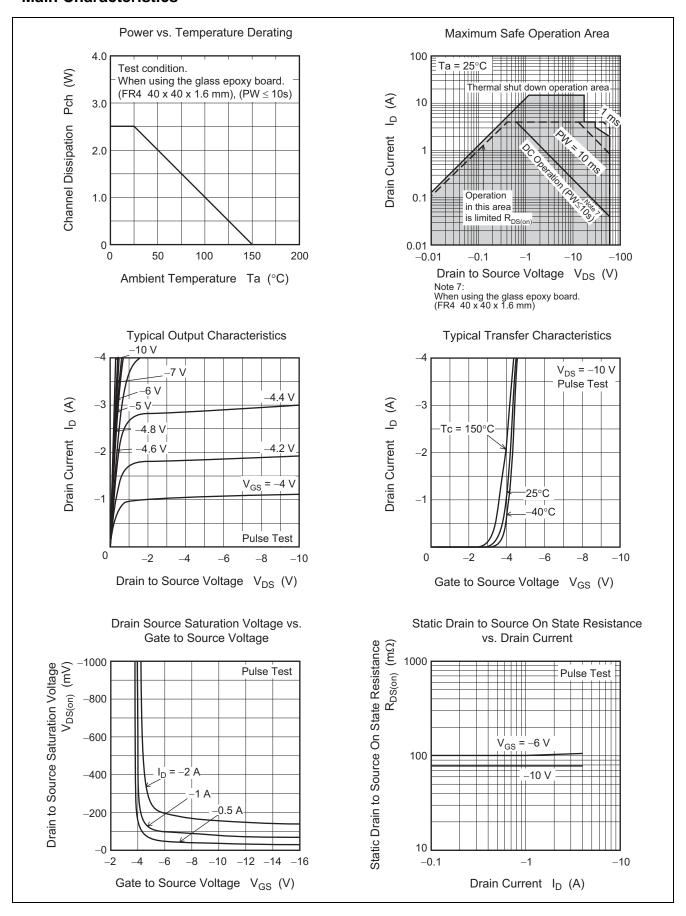
 $(Ta = 25^{\circ}C)$

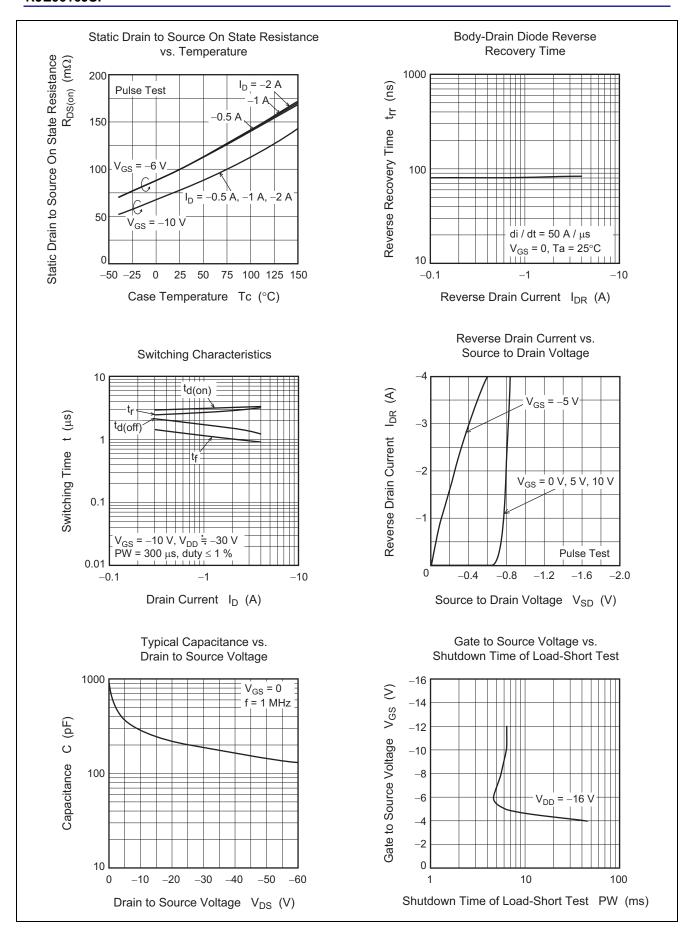
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I _{D1}		_	-4	Α	$V_{GS} = -3.5 \text{ V}, V_{DS} = -10 \text{ V}$
	I _{D2}	-	_	-10	mA	$V_{GS} = -1.2 \text{ V}, V_{DS} = -10 \text{ V}$
	I _{D3}	-4	_	_	Α	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{\text{Note 5}}$
Drain to source breakdown voltage	V _{(BR)DSS}	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown	$V_{(BR)GSS}$	-16	_	_	V	$I_G = -800 \mu\text{A}, V_{DS} = 0$
voltage	V _{(BR)GSS}	2.5	_	_	V	$I_G = 100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS1}	_	_	-100	μΑ	$V_{GS} = -8 \text{ V}, V_{DS} = 0$
	I _{GSS2}	_	_	-50	μΑ	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
	I _{GSS3}	_	_	-1	μΑ	$V_{GS} = -1.2 \text{ V}, V_{DS} = 0$
	I _{GSS4}	_	_	100	μΑ	$V_{GS} = 2.4 \text{ V}, V_{DS} = 0$
Input current (shut down)	I _{GS(OP)1}	_	-0.8	_	mA	$V_{GS} = -8 \text{ V}, V_{DS} = 0$
	I _{GS(OP)2}	_	-0.35	_	mA	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS1}	-	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS2}	_	_	-10	μΑ	$V_{DS} = -48 \text{ V}, V_{GS} = 0,$ $Ta = 125^{\circ}\text{C}$
Gate to source cutoff voltage	V _{GS(off)}	-2.2	_	-3.4	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Static drain to source on state	R _{DS(on)}		102	150	mΩ	$I_D = -2 \text{ A}, V_{GS} = -6 \text{ V}^{\text{Note 5}}$
resistance	R _{DS(on)}	-	77	90	mΩ	$I_D = -2 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 5}}$
Output capacitance	Coss	_	290	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$
Turn-on delay time	t _{d(on)}	_	3.20	_	μs	$V_{GS} = -10 \text{ V}, I_{D} = -2 \text{ A},$
Rise time	t _r	_	2.80	_	μs	$R_L = 15 \Omega$
Turn-off delay time	t _{d(off)}	_	1.55	_	μs	
Fall time	t _f	_	1.05	_	μs	
Body-drain diode forward voltage	V_{DF}	_	-0.84	_	V	$I_F = -4 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}	_	84	_	ns	$I_F = -4 \text{ A}, V_{GS} = 0$ $di_F/dt = 50 \text{ A}/\mu\text{s}$
Over load shut down operation time Note 6	t _{os1}	_	6.34	_	ms	$V_{GS} = -5 \text{ V}, V_{DD} = -16 \text{ V}$

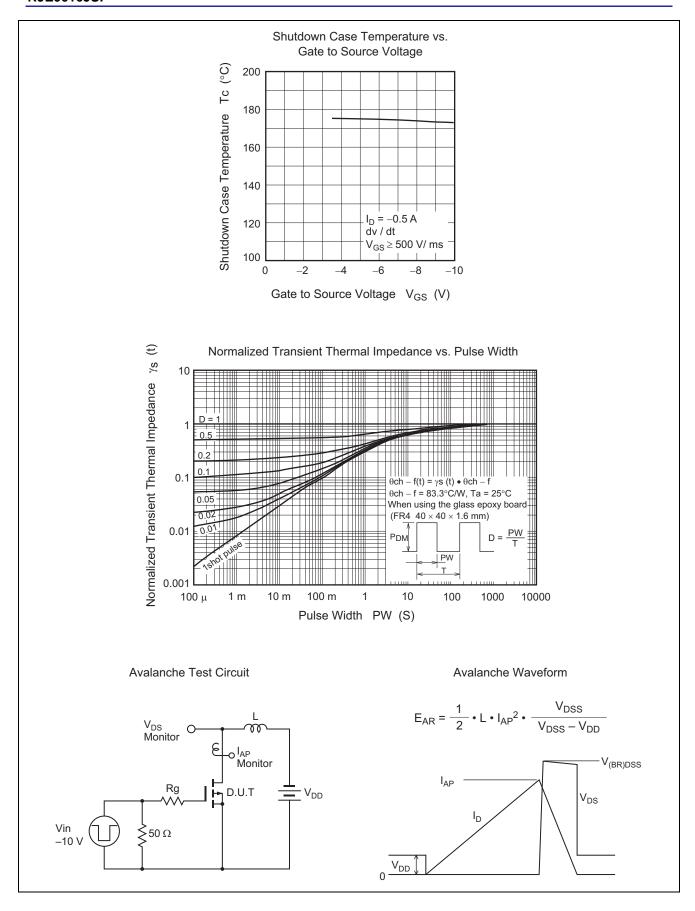
Notes: 5. Pulse test

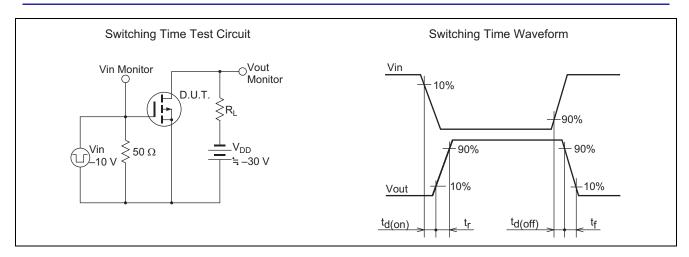
6. Including the junction temperature rise of the over loaded condition.

Main Characteristics

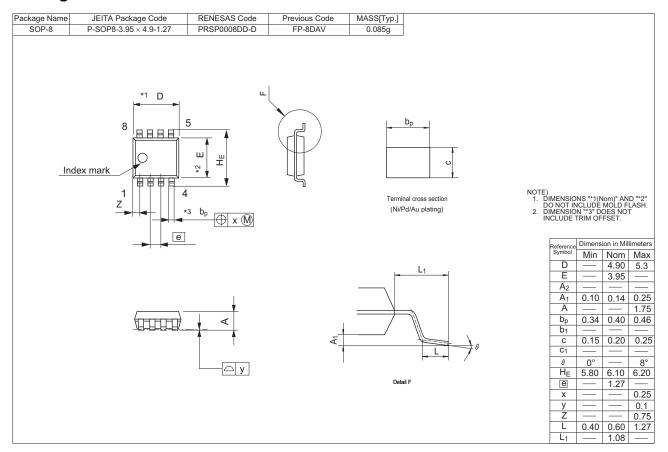








Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJE0616JSP-00-J0	2500 pcs	Taping

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