

**RENESAS** Using Flash as the Root File System

### Notes

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### Introduction

IDT provides a royalty free, license-fee free Linux operating system environment for its Interprise™ family of integrated communications processors. In addition to the basic operating system and device drivers, IDT provides higher level functions common in gateways and other applications. This application note describes how to use the file systems residing in flash devices as root file systems.

For more details on IDT Linux, please refer to: http://www.idt.com/products/docs/Linux\_ds.pdf.

### Overview

The majority of IDT evaluation boards, as well as end products deployed in the field by users of IDT processors, have one or more flash devices on the system. These flash devices can be used to store Linux kernel images, Linux applications or both. IDT Linux supports a variety of flash devices and file systems suitable for flash devices. It also provides a feature which allows the file system residing within the flash to be mounted as the "root" file system. This feature is very useful in systems that have limited main memory (typically SDRAM or SRAM). An alternative would be to use "ramdisk" as the "root" file system. However, "ramdisk" consumes main memory and leaves very little space for executing kernel and applications. For example, on systems that have 16 MB of main memory, if 8 MB is used for "ramdisk", less than 8 MB will be available for running kernel and applications.

# Flash Devices on IDT's 79EB438 Evaluation Board

IDT's EB438 evaluation board has two flash memory devices on the board. This document explains how to create Journaling Flash File System, version 2 (JFFS2) on a flash device and mount it under Linux as the "root" file system. This document uses the EB438 evaluation board as an example. However, the concepts described here are applicable to any system which uses an IDT processor running IDT Linux.

The EB438 evaluation board has two flash memory devices on the board as shown in Table 1.

Flash	Size (MB)	Port Width (bits)	Physical Address (hex)
AM29DL163DT	2	16	08000 000
AM29DL 323DT	4	16	11000 000

Table 1 EB438 Flash Memory Devices

The board also has an EEPROM located at address 0x1FC00000 - the power reset address. Therefore, boot code such as IDT/sim or IdtBoot resides within this EEPROM. In this document, the 4 MB flash will be used for storing the file system

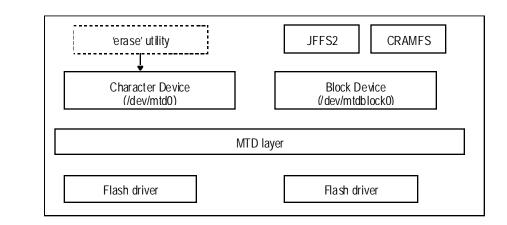
# Memory Technology Devices (MTD) Overview

IDT Linux provides device drivers for a variety of flashes. It also provides an intermediate software layer which offers an interface through which upper layers of software such as file systems can interact with device drivers at the lower level. This intermediate layer is called MTD (Memory Technology Devices). The

#### IDT Application Note AN-449

## Notes

advantage of this approach is that the upper layer does not need to know the specifications of the device used to store data/files and the device driver can remain completely unaware of the needs of the application using the data/files. A simplified view of the software layers is shown in the figure below.



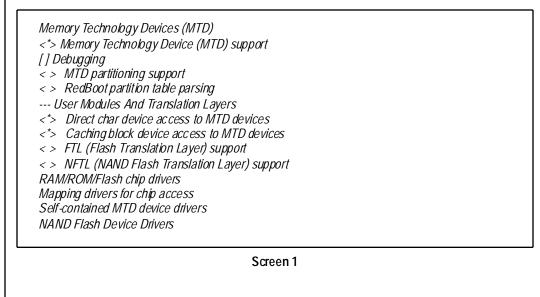
# Journaling Flash File System, Version 2 (JFFS2)

JFFS2 is a file system suitable for embedded devices and is the one we will discuss in this document. It is a compressed file system like CRAMFS (Compressed ROM File System). Unlike CRAMFS, which is read-only, JFFS2 is a read-write file system. For more details on JFFS2, please refer to <a href="http://sources.redhat.com/jffs2/">http://sources.redhat.com/jffs2/</a>.

## Kernel Configuration for MTD and JFFS2

The remainder of this document assumes that the reader is reasonably familiar with IDT Linux and has hands-on experience with it. Many routine steps in the build and test procedure have been skipped here in order to simplify this document and to focus on the issues of interest to system designers. For a detailed description of the skipped steps, please refer to the IDT Linux release documents you received with the Linux product.

While configuring the kernel for the EB438 processor, select the "Memory Technology Devices (MTD)" option from the main menu to display the screen shown below. Enable the MTD options on this screen as indicated by the asterisks.



screen. Enter the values that are underlined. (*) CFI Flash device in physical memory map ( <u>11000000<sup>1</sup></u> ) Physical start address of flash mapping ( <u>400000<sup>2</sup></u> ) Physical length of flash mapping ( <u>2<sup>1</sup></u> ) Bus width in octets Screen 3 Notes 1, 2, 3 above: The values in the example shown above are specific to EB438 evaluation <sup>1</sup> This address will depend on board design and configuration. 11000000 shown above is for th of the 4MB flash on the EB438. <sup>2, 3</sup> This will change depending on the size and type of the flash used.	Notes	Now select "RAM/ROM/Flash chip drivers" option in Screen 1 to display the Screen 2, shown to Enable the options on this screen as indicated by the asterisks.		
Return to Screen 1 and select the "Mapping drivers for chip access" option to display the screen. Enter the values that are underlined.         <*> CFI Flash device in physical memory map (11000007) Physical start address of flash mapping (4000007) Physical start address of flash mapping (27) Bus width in octets         Screen 3         Notes 1, 2, 3 above: The values in the example shown above are specific to EB438 evaluation <sup>1</sup> This address will depend on board design and configuration. 11000000 shown above is for th of the 4MB flash on the EB438.         2.3 This will change depending on the size and type of the flash used.         Now enable support for JFFS2 file system in kernel under "File Systems" menu, shown in th below.         File Systems		<ul> <li>&lt;&gt; Detect non-CFI AMD/JEDEC-compatible flash chips</li> <li>[] Flash chip driver advanced configuration options</li> <li>&lt;&gt; Support for Intel/Sharp flash chips</li> <li>&lt;&gt; Support for AMD/Fujitsu flash chips</li> <li>&lt;&gt; Support for RAM chips in bus mapping</li> <li>&lt;&gt; Support for ROM chips in bus mapping</li> <li>&lt;&gt; Support for absent chips in bus mapping</li> </ul>		
screen. Enter the values that are underlined.		Screen 2		
(1100000 <sup>0</sup> ) Physical start address of flash mapping         (400000 <sup>2</sup> ) Physical length of flash mapping         (2 <sup>2</sup> ) Bus width in octets         Screen 3         Notes 1, 2, 3 above: The values in the example shown above are specific to EB438 evaluation         1 This address will depend on board design and configuration. 11000000 shown above is for th of the 4MB flash on the EB438.         2,3 This will change depending on the size and type of the flash used.         Now enable support for JFFS2 file system in kernel under "File Systems" menu, shown in the below.         File Systems		Return to Screen 1 and select the "Mapping drivers for chip access" option to display the folk		
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[*] Journalling Flash File System v2 (JFFS2) support		File Systems		
Screen 4				
		Since JFFS2 on flash will be used as the root file system, the ramdisk support may be disabled I checking the "Block Devices -> Ramdisk" option. Save the configuration. We are ready to build the keep of the second secon		

#### IDT Application Note AN-449

Notes

#### Building a JFFS2 File System

In order to build a JFFS2 file system, a utility called "mkfs.jffs2" is required. This utility is generally available in the public domain, possibly on your desktop system. It is also provided by IDT in releases after March 17, 2004.

The procedure for building JFFS2 file system is given below.

- 1. On your development system host, create a new directory and copy the files that you wish to have within your flash root system into that directory. Let's call this directory "stage".
- 2. Run the mkfs.jffs2 utility. The syntax for mkfs.jffs2 is as follows:

mkfs.jffs2 -r <directory> -o <outfile> [-1 | -b] [-p] [-e]

where:

*directory:* The directory created in step 1 above

outfile: The file that can be programmed to flash

-l or -b: Little or Big Endian respectively

-p: pad to end of the block

-e: erase block size.

Issue the command "mkfs.jffs2-help" for details on available options.

In our case, since we are going to implement the root file system on the flash, the command can be used as shown below:

mkfs.jffs2 -r stage -o root.jffs2 -p

This will generate root.jffs2 under the current directory. Make sure that the "stage" directory contains all the required files for the root file system.

### **Programming Flash**

Using IDT/sim or IdtBoot, the file created above can be programmed into the flash on the board using the *fb* command as shown below:

fb -f 0xb100000 root.jffs2

This command will program the file 'root.jffs2' which has the JFFS2 file system into the second flash (4MB) on board.

### **Booting Linux**

First, the kernel needs to be made aware that the flash is to be used as the root file system. This can be achieved by setting boot parameters under IDT/sim or Idt Boot as shown below:

<IDT>set bootparm1 "root=/dev/mtdblock0 rootfstype=jffs2"

Next, the Linux image can be booted. After linux is booted, typing the *mount* command will show the devices that are mounted. The result should be as shown below:

/dev/mtdblock0 on / type jffs2 (rw)

/proc on / proc type proc (rw)

The first line shows that the root device is mounted on */dev/mtdblock0*. You may also run the *free* command to see how much memory remains available in the system. It will approximate the actual physical memory available.

Notes

#### Conclusion

By following the steps described in this document, any system using an IDT processor running IDT Linux will be able to use file systems residing in the flash devices as the root file system, allowing system developers to fully utilize the main memory for running kernel and applications.

### References

http://www.linux-mtd.infradead.org/

http://sources.redhat.com/jffs2/

IDT 79EB438 Evaluation Board Manual

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