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# M32C/82,83 Group

# Time measurement function of Intellingent I/O Group 0 and 1 with prescaler function

#### Abstract

This application note describes the time measurement function of Intelligent I/O group 0 with prescaler function, which measures a period from the beginning of the program to input of any edge.

#### 2. Introduction

This application note is applied to the M32C/83 group microcomputer.

This program can also be used when operating other microcomputers within M16C family, provided they have the same SFR (Special Function Registers) as the M32C/83 group. However, some functions may have been modified. Refer to the User's Manual for details. Use functions covered in this Application Note only after careful evaluation.

#### 3. Detailed description

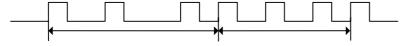
Intelligent I/O group 0 and 1 are composed of one 16-bit Base Timer for free-run operation and eight 16-bit registers for the time measurement function and waveform generation function.

Channel 6 and 7 have the prescaler function.

In this example, using the time measurement function of channel 6, a period is measured from the beginning of the program to input to INPC06 port of any rising edge.

## Input pulse waveform

(Detecting a rising edge when the value of prescaler is "2")



Measuring a period from the beginning of the program to 3rd rising edge

When using prescaler function, trigger input is counted.

The period is measured every trigger input of setting value in the GiTPR6 register +1.

#### (1) Time measurement function setting

This example will use Channel 6 in Group 0 for the time measurement function. Select the rising edge mode for the trigger of the measurement.

#### (2) Time measurement calculation

Use the time measurement interrupt of Channel 6. Then, read the value of register G0TM6 during the interrupt routine. Then calculate the difference from the value that is measured last time. This is the measuring time.

### ime measurement function of Intellingent I/O Group 0 and 1 with prescaler function

#### 3.1 How to set up

This section shows the setting procedures and setting values to proceeds section "3. Detailed Description". For detail configuration of each register, please refer to M32C/83 Group HARDWARE MANUAL.

#### (1) Disabling an Interrupt

Set I flag=0. Or set bits ILV2 to 0=000<sub>2</sub> in register IIOkIC (k=0 to 11) where the interrupt request of the Intelligent I/O is assigned.

#### (2) G2BCR0 register 0 This setting supplies the BTSR register with a clock, and enables BCK1to 0 Count source select bit the register to be set. DIV4 to 0 Count source division ratio select bit No division Don't use. Set to "0". This setting resets Base Timers in Groups 0 to 3. Base Timer (3) BTSR register of Group i starts counting from 0000<sub>16</sub> by setting the BTS bit in 0 0 0 the register GiBCR1 to "1" after the operation clock of Base Timer is set in GiBCR0 register. BT3S to BT0S Base timer start bit Resets the base timer of group 0 to 3. Don't use. Set to "00002" (4) G2BCR0 register Stop a clock supplied to Group 2 0 0 0 0 0 0 0 0 0 when Group 2 and BTSR register are not used. BCK1 to 0 Count source select bit Stops a clock Don't use. Set to "0000002" (5) GiBCR0 register This setting supplies the registers (6) -(13) with a clock. Set each register to "011111112" in order to be effective after setting. BCK1 to 0 Count source select bit

DIV4 to 0

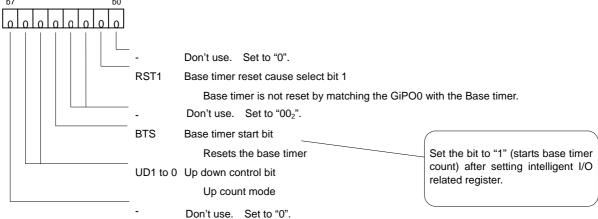
Count source division ratio select bit

No division

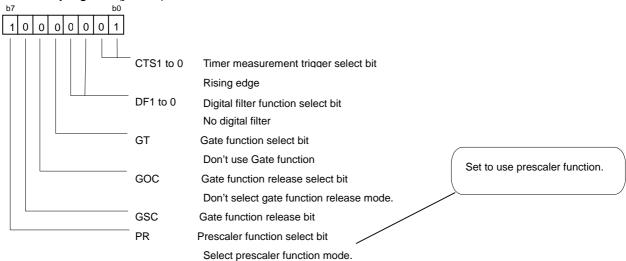
Don't use. Set to "0".



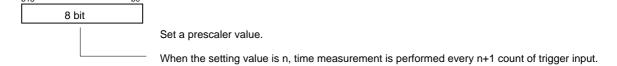




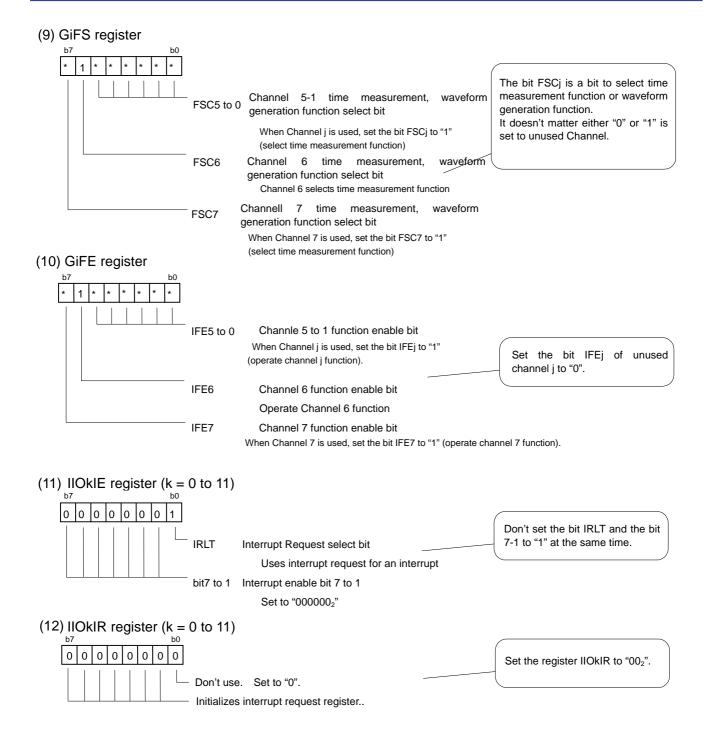
#### (7) G0TMCRj register (j = 6,7)



#### (8) G0TPRj register (j =6,7)

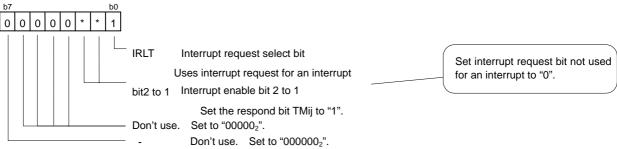




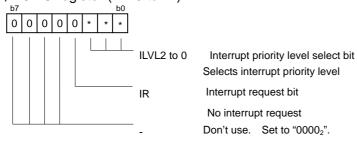






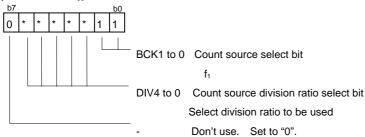


#### (14) IIOkIC register (k = 0 to 11)

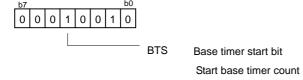


- (15) PSC register, PSLa register (a=0 to 3), PSb register (b=0 to 9), IPS register Set the port INPCij
- (16) Interrupt enable flag (I flag = "1")

#### (17) GiBCR0 register



## (18) GiBCR1 register



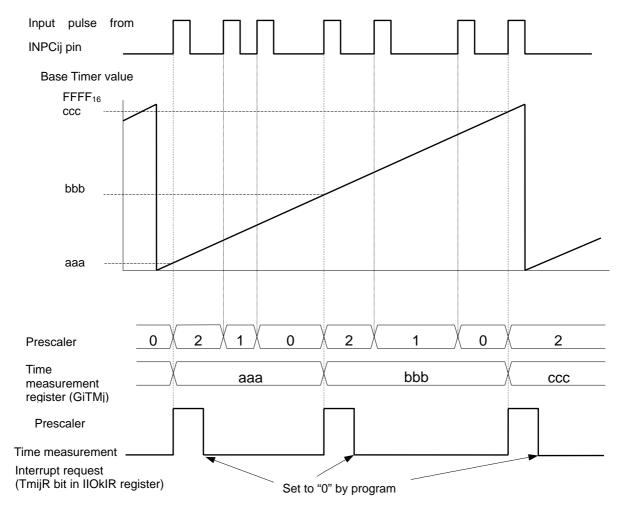


#### 3.2 Precaution on Interrupts

During the Intelligent I/O interrupt routine, the II0kIR register corresponding to this interrupt must be set to "00<sub>16</sub>" (initialize). If this setting is missing, the IR bit in the II0kIC register is not set to "1" regardless of the Intelligent I/O interrupt request. (No interrupt occurs.)

#### 3.3 Timing chart

The below is a timing diagram with setting according to the section 3.1 and 3.2.



Note: In this diagram, PR bit in GiTMCRj register is set to "1". (Use prescaler function.)



#### 4. The example of a reference program

```
/*FILENAME: rej05b0394 src.c
       : 1.00
/*FUNCTION: Time measurement function of
/* Intelligent I/O Group 0 and 1 with prescaler function */
/*****************/
   include file */
/************/
#include <stdio.h>
#include "sfr32c83.h"
/*****************************/
  Function Definition */
                                                              /* Interrupt function */
void ch6_int();
#pragma INTERRUPT ch6_int
/************/
      Global variable Definition
static short palse;
/******************************/
     main
/***********/
void main()
        /* main clock set */
                                            /* protect off */
        prc0 = 1;
                                            /* Main clock : No division */
         mcd
                 = 0x12;
         prc1 = 0;
                                            /* protect on */
        /* iio group0 initial set */
         g2bcr0 = 0x7f;
        \begin{array}{ll} \text{btsr} &= 0\text{x}00;\\ \text{g}2\text{bcr}0 &= 0\text{x}00; \end{array}
                                            /* all base timer stop */
                                            /* group2 clock stop */
        g0bcr0 = 0x7f;
                                            /* b0,b1: count source:f1
                                              b2 to b6:count source division ratio: No division */
         g0bcr1 = 0x00;
         g0tmcr6 = 0x81;
                                            /* b0,b1: rising edge
                                              b2,b3: No digital filter
                                                     Don't use gate function
                                              b4:
                                              b5:
                                                     Don't select function release mode
                                              b6:
                                                     Gate function release bit
                                              b7:
                                                     Use prescaler function
         g0tpr6 = 0x02;
                                                     Set prescaler period */
                                                     ch6 : Time measurement function */
         g0fs
                 = 0x40;
                                                     ch0,6: Operate function */
         g0fe
                 = 0x40;
        /* iio group0 interrupt initial set
                                            /* Latch interrupt request */
         iio6ie = 0x01;
                                            /* Clear interrupt request flag */
         iio6ir = 0x00;
         iio6ie = 0x03:
                                            /* Enable interrupt of request flag corresponding to interrupt */
         iio6ic = 0x03;
                                            /* Select interrupt priority level */
        /* port set */
```



# Time measurement function of Intellingent I/O Group 0 and 1 with prescaler function

```
pd15 = 0x00;
                                               /* INPC0_6 input */
                   = 0;
         ps2
         /* interrupt enable */
         _asm("fset i");
         /* iio group0 basetimer start */
         bts_g0bcr1
         while(1);
}
/* iio ch6 interrupt */
void ch6_int()
         static signed short old_tr = 0;
         short signed now_tr;
         iio6ir = 0x00;
                                                       /* Clear interrupt request */
         now_tr = (signed short)g0tm6;
                                                       /* Read time measurement register */
         palse = now_tr - old_tr;
                                                       /* Pulse width measurement */
         old_tr = now_tr;
                                                       /* Store the value of current register */
             ----- program end */
```



#### 5. **Example Waveform and Result**

The following example shows measuring a pulse width at pin INPC06 by using the Intelligent I/O Group 0.

Conditions: Supply voltage = 5V,

Main clock (Xin) = 10MHz

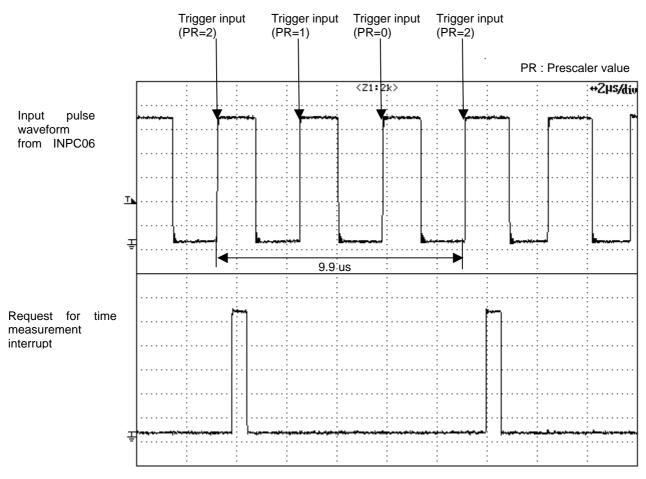
Base Timer operation clock (fBT) = 10MHz

Input pulse = 300kHz

Prescaler = 2

Result counts: 99

 $(99 / 10MHz = 9.9 \mu s)$ 



Measurement result from oscilloscope



#### 6. Reference

HADWARE MANUAL Refer to the M32C/83 group HARDWARE MANUAL.

#### 7. Web-site and contact for support

Renesas Web-site

http://www.renesas.com

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