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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# JUNCTION FIELD EFFECT TRANSISTOR 2SK3718

# N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR FOR IMPEDANCE CONVERTER OF ECM

#### **DESCRIPTION**

The 2SK3718 is suitable for converter of ECM.

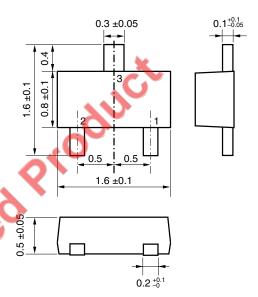
#### **FEATURES**

- · Low noise
  - NV = -117 dB TYP. (VDS = 4.5 V, C = 10 pF, RL = 1.0 k $\Omega$ )
- Especially suitable for telephone, cellular phone & audio
- Small package SC-89 (TUSM)

#### ORDERING INFORMATION

PART NUMBER	PACKAGE			
2SK3718	SC-89 (TUSM)			

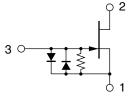
# PACKAGE DRAWING (Unit: mm)



## ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = $-1.0 \text{ V}$ )	VDSX	20	V
Gate to Drain Voltage	V <sub>GDO</sub>	-20	V
Drain Current	ΙD	10	mA
Gate Current	lg	10	mA
Total Power Dissipation	Рт	100	mW
Junction Temperature	$T_{j}$	125	°C
Storage Temperature	$T_{stg}$	-55 to +125	°C

#### **EQUIVALENT CIRCUIT**



- 1: Source
- 2: Drain
- 3: Gate

Caution Please take care of ESD (Electro Static Discharge) when you handle the device in this document.

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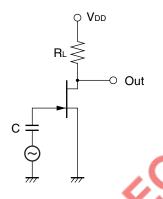
## **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Cut-off Current	IDSS	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0 V	90	250	430	μА
Gate Cut-off Voltage	V <sub>GS(off)</sub>	$V_{DS} = 5.0 \text{ V}, I_{D} = 1.0 \mu\text{A}$		-0.37	-1.0	<b>V</b>
Forward Transfer Admittance	<b>y</b> fs1	$V_{DS}$ = 5.0 V, $I_{D}$ = 30 $\mu$ A, f = 1.0 kHz	300	480		μS
	<b>y</b> fs2	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0 V, f = 1.0 kHz	750	1600		μS
Input Capacitance	Ciss	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz		3.9		pF
Voltage Gain	Gv	$V_{DD}$ = 4.5 V, C = 10 pF, R <sub>L</sub> = 1 k $\Omega$ ,		-1.3		dB
		V <sub>IN</sub> = 10 mV, f = 1 kHz				
Noise Voltage	NV1	$V_{DD}$ = 2.0 V, C = 5 pF, R <sub>L</sub> = 2.2 k $\Omega$ ,		-109.5		dB
		A-curve				
	NV2	$V_{DD}$ = 4.5 V, C = 10 pF, R <sub>L</sub> = 1 k $\Omega$ ,		-117	-112	dB
		A-curve	. C			

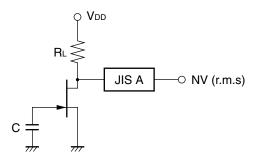
#### **IDSS CLASSIFICATION**

MARKING	AE	AF	АН	AJ		
Ioss (μA)	90 to 180	150 to 240	210 to 350	320 to 430		
VOLTAGE GAIN	I TEST CIRCUIT		ced.			
○ V <sub>DD</sub>			N			
1	Out	70				
c =						
	$^{\prime}O^{\vee}$					

## **VOLTAGE GAIN TEST CIRCUIT**



## NOISE VOLTAGE TEST CIRCUIT

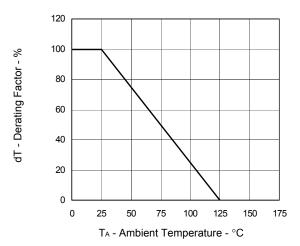


les - Gate to Source Current - μA

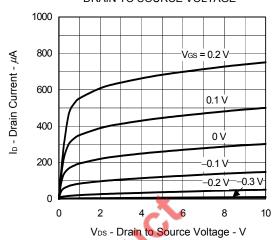
Ciss - Input Capacitance - pF

## TYPICAL CHARACTERISTICS (TA = 25°C)

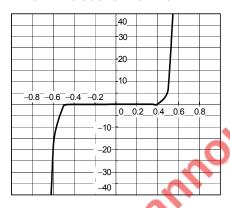
#### DERATING FACTOR OF POWER DISSIPATION



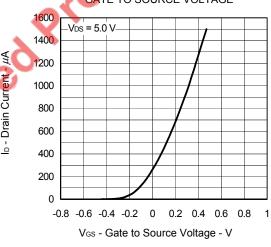
# DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



# GATE TO SOURCE CURRENT vs. GATE TO SOURCE VOLTAGE

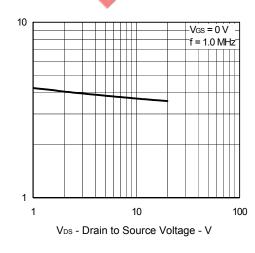


DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

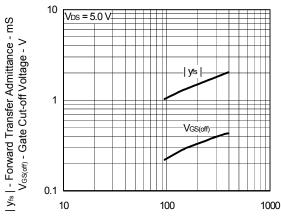


V<sub>GS</sub> - Gate to Source Voltage - V

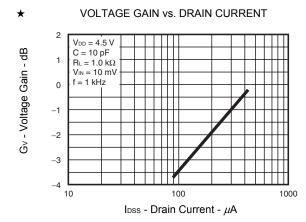


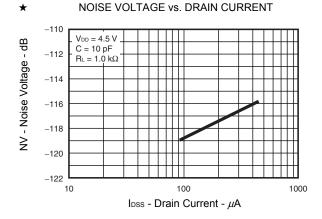


FORWARD TRANSFER ADMITTANCE AND GATE CUT-OFF VOLTAGE vs. ZERO GATE VOLTAGE DRAIN CURRENT



IDSS - Zero Gate Voltage Drain Current - μA





loss - Drain Cur

loss - Drain Cur

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