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# HZN6.2Z4MFA

## Silicon Planar Zener Diode for Surge Absorb

REJ03G0204-0100Z Rev.1.00 Mar.29.2004

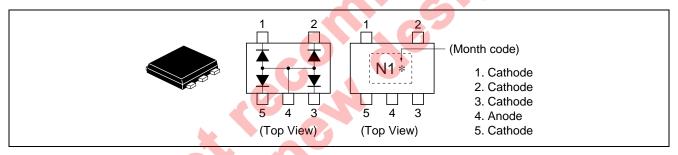
#### **Features**

- HZN6.2Z4MFA has four devices in a monolithic, and can absorb surge.
- Low capacitance (C = 4.0 pF Typ / 4.5 pF max) and can protect ESD of signal line.
- VSON-5T Package is suitable for high density surface mounting.

#### **Ordering Information**

Type No.	Laser Mark	Package Code
HZN6.2Z4MFA	N1	VSON-5T

#### **Pin Arrangement**



#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit	
Power dissipation	Pd *	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	−55 to +150	°C	

Note: Four device total, See Fig.2.

### **Electrical Characteristics** \*1

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	$V_Z$	5.90	_	6.50	V	$I_Z = 5$ mA, 40 ms pulse
Reverse current	I <sub>R</sub>	_	_	3	μА	V <sub>R</sub> = 5.5 V
Capacitance	С	_	4.0	4.5	pF	$V_R = 0 V$ , $f = 1 MHz$
Dynamic resistance	r <sub>d</sub>	_	_	60	Ω	$I_Z = 5 \text{ mA}$
ESD-Capability *2	_	8	_	_	kV	C = 150 pF, R = 330 $\Omega$ , Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ;  $I_R > 3 \mu A$  at  $V_R = 5.5 V$ .

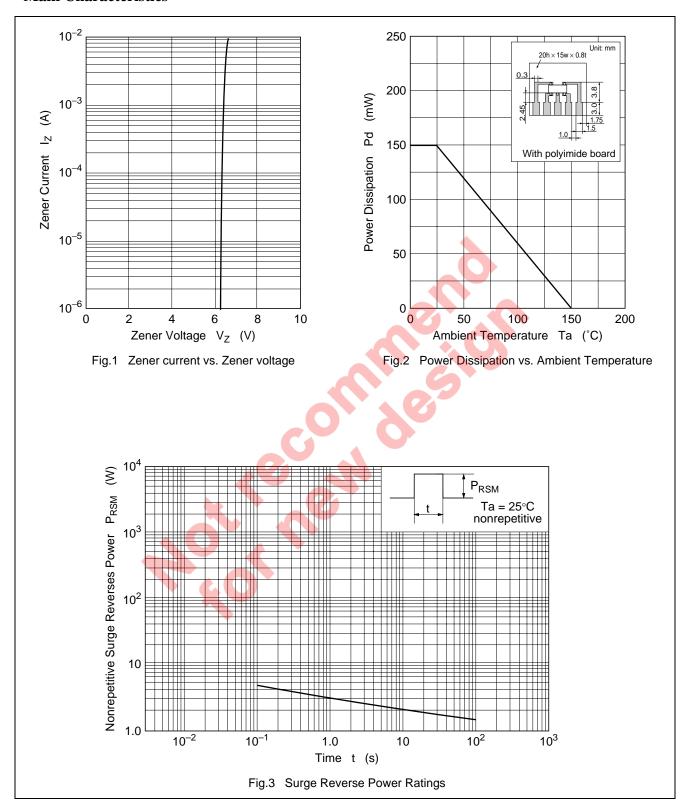
3. Between cathode and anode.

#### **Month Code**

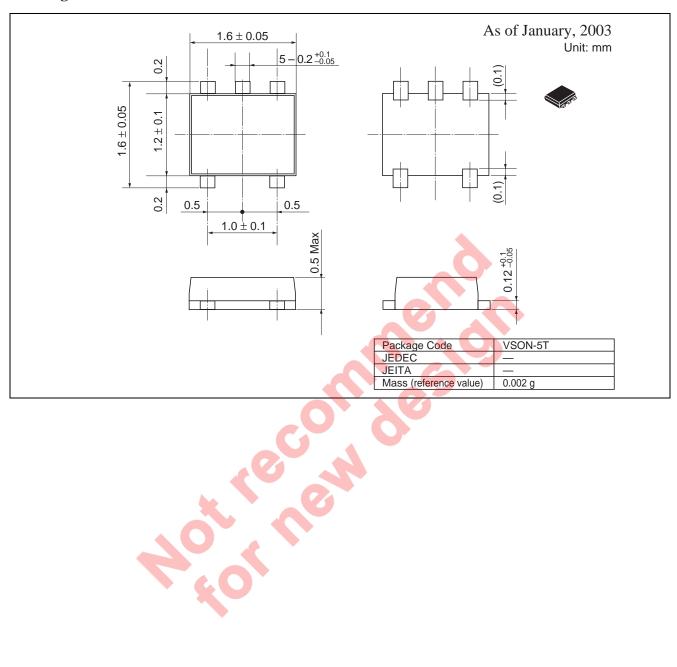
	Assemble	e
Month of Manufacture	JAPAN	MALAYSIA
January	A	1 0
February	В	2
March	C	3
April	D	4
May	E	5
June	F	6

	reverse directi	on 10 pulse	
= 5.5 V.	4,10		
CO	962	Assemble	
ALAYSIA	Month of Manufacture	JAPAN	MALAYSIA
		_	
	July	G	7
	August	H	7 8
1			<u> </u>
	August	Н	8
	August September	H J	8 9

#### **Main Characteristics**



### **Package Dimensions**



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