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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# HZN6.8Z4MFA

# Silicon Planar Zener Diode for Surge Absorb

REJ03G0205-0100Z Rev.1.00 Mar.29.2004

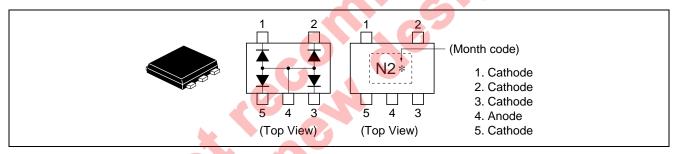
#### **Features**

- HZN6.8Z4MFA has four devices in a monolithic, and can absorb surge.
- Low capacitance (C = 4.0 pF Typ / 4.5 pF max) and can protect ESD of signal line.
- VSON-5T Package is suitable for high density surface mounting.

#### **Ordering Information**

Type No.	Laser Mark	Package Code
HZN6.8Z4MFA	N2	VSON-5T

### **Pin Arrangement**



#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit	
Power dissipation	Pd *	150	mW	_
Junction temperature	Tj	150	°C	_
Storage temperature	Tstg	−55 to +150	°C	

Note: Four device total, See Fig.2.

## **Electrical Characteristics** \*1

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	$V_Z$	6.47	_	7.00	V	$I_Z = 5$ mA, 40 ms pulse
Reverse current	I <sub>R</sub>	_	_	2	μΑ	V <sub>R</sub> = 3.5 V
Capacitance	С	_	4.0	4.5	pF	$V_R = 0 V$ , $f = 1 MHz$
Dynamic resistance	r <sub>d</sub>	_	_	30	Ω	$I_Z = 5 \text{ mA}$
ESD-Capability *2	_	8	_	_	kV	$C = 150 \text{ pF}, R = 330 \Omega$ , Both forward and
						reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ;  $I_R > 2~\mu\text{A}$  at  $V_R$  = 3.5 V.

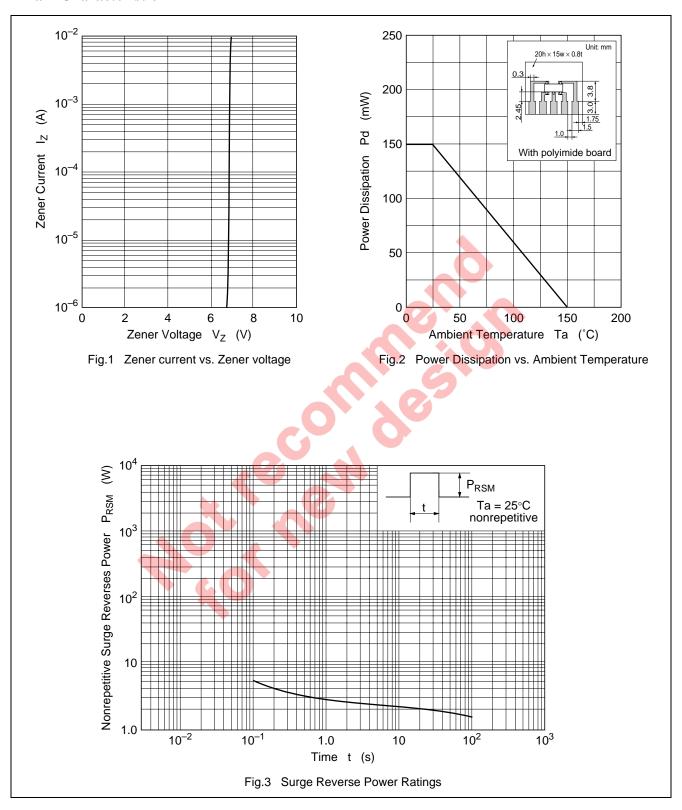
3. Between cathode and anode.

#### **Month Code**

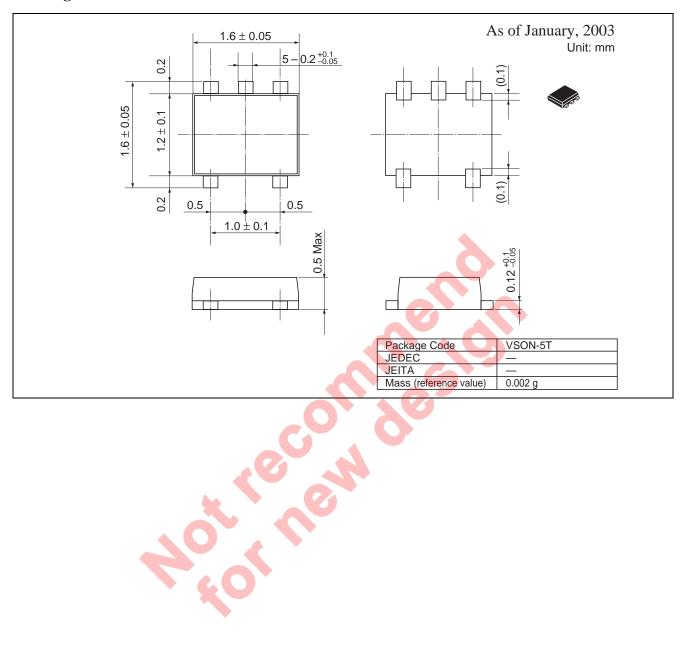
	Assemble	
Month of Manufacture	e JAPAN	MALAYSIA
January	A	1
February	В	2
March	С	3
April	D	4
May	E	5
June	F	6

	reverse direction	on 10 pulse	
= 3.5 V.	1969		
		Assemble	
MALAYSIA	Month of Manufacture	JAPAN	MALAYSIA
MALAYSIA 1	Month of Manufacture July		MALAYSIA 7
MALAYSIA 1 2		JAPAN	
1	July	<b>JAPAN</b> G	7
1 2 3 4	July August	JAPAN G H	7 8
2 3	July August September	JAPAN G H J	7 8 9

#### **Main Characteristic**



## **Package Dimensions**



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