

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-R8C-A063A/E	Rev.	1.00
Title	Caution on using wait mode		Information Category	Technical Notification	
Applicable Product	R8C/10, R8C/11, R8C/12, R8C/13, R8C/14, R8C/15, R8C/16, R8C/17, R8C/18, R8C/19, R8C/1A, R8C/1B	Lot No.	Reference Document	User's Manual: Hardware of applicable products	
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This document describes corrections of usage condition regarding $\overline{\text{INT3}}$ interrupt in "Interrupts Used to Exit Wait Mode and Usage Conditions" which is described in the User's Manual: Hardware for the applicable products.

1. Correction contents of R8C/10, R8C/11 and R8C/12 group

Applicable Product	Applicable User's Manual: Hardware	Applicable Page
R8C/10	REJ09B0019-0120(Rev.1.20)	Page 25 of 180
R8C/11	REJ09B0062-0120(Rev.1.20)	Page 39 of 204
R8C/12	REJ09B0110-0120(Rev.1.20)	Page 25 of 181

Note. If the description to be corrected appears in the User's Manual on pages other than pages listed above, the same correction will also apply to the descriptions on those pages.

Correction contents of the R8C/10, R8C/11 and R8C/12 group are shown below.

Incorrect: **Bold**; Correct: **Gray hatched**

Incorrect:

Table 6.3 Interrupts Used to Exit Wait Mode and Usage Conditions

Interrupt	When CM02 = 0	When CM02 = 1
Serial interface interrupt	Can be used when operating with internal or external clock	Can be used when operating with external clock
(Omission)	(Omission)	(Omission)
INT interrupt	Can be used	Can be used (INT0 and INT3 can be used if there is no filter.)
(Omission)	(Omission)	(Omission)

Correct:

Table 6.3 Interrupts Used to Exit Wait Mode and Usage Conditions

Interrupt	CM02 = 0	CM02 = 1
Serial interface interrupt	Can be used when operating with internal or external clock	Can be used when operating with external clock
(Omission)	(Omission)	(Omission)
INT0 to INT2 interrupt	Can be used	Can be used (INT0 can be used if there is no filter.)
INT3 interrupt	Can be used	- (Do not use)
(Omission)	(Omission)	(Omission)

2. Correction contents of R8C/13 group

Applicable Product	User's Manual: Hardware of applicable	Applicable Page
R8C/13	REJ09B0111-0120(Rev.1.20)	Page 39 of 205

Note. If the description to be corrected appears in the User's Manual on pages other than pages listed above, the same correction will also apply to the descriptions on those pages.

Correction contents of the R8C/13 group are shown below.

Incorrect: Bold; Correct: Gray hatched

Incorrect:

Table 6.3 Interrupts Used to Exit Wait Mode and Usage Conditions

Interrupt	When CM02 = 0	When CM02 = 1
Serial interface interrupt	Can be used when operating with internal or external clock	Can be used when operating with external clock
(Omission)	(Omission)	(Omission)
INT interrupt	Can be used	Can be used (INT0 and INT3 can be used if there is no filter.)
(Omission)	(Omission)	(Omission)

Correct:

Table 6.3 Interrupts Used to Exit Wait Mode and Usage Conditions

Interrupt	CM02 = 0	CM02 = 1
Serial interface interrupt	Can be used when operating with internal or external clock	Can be used when operating with external clock
(Omission)	(Omission)	(Omission)
INT0 to INT2 interrupt	Can be used	Can be used (INT0 can be used if there is no filter.)
INT3 interrupt	Can be used	Can be used (Can be used when there is no filter and Timer C output compare mode (the TCC13 bit in the TCC1 register is set to "1")
(Omission)	(Omission)	(Omission)

3. Correction contents of R8C/14, R8C/15, R8C/16, R8C/17, R8C/18, R8C/19, R8C/1A and R8C/1B group

Applicable Product	User's Manual: Hardware of applicable	Applicable Page
R8C/14,15	REJ09B0164-0210(Rev.2.10)	Page 50 of 253
R8C/16,17	REJ09B0169-0210(Rev.2.10)	Page 50 of 254
R8C/18,19	REJ09B0222-0130(Rev.1.30)	Page 65 of 233
R8C/1A,1B	REJ09B0252-0130(Rev.1.30)	Page 70 of 315

Note. If the description to be corrected appears in the User's Manual on pages other than pages listed above, the same correction will also apply to the descriptions on those pages.

Correction contents of the R8C/14, R8C/15, R8C/16, R8C/17, R8C/18, R8C/19, R8C/1A and R8C/1B group are shown below.

Incorrect: **Bold**; Correct: **Gray hatched**

Incorrect:

Table Interrupts Used to Exit Wait Mode and Usage Conditions

Interrupt	When CM02 = 0	When CM02 = 1
Serial interface interrupt	Usable when operating with internal or external clock	Usable when operating with external clock
(Omission)	(Omission)	(Omission)
INT interrupt	Usable	Usable (INT0 and INT3 can be used if there is no filter.)
(Omission)	(Omission)	(Omission)

Correct:

Table Interrupts Used to Exit Wait Mode and Usage Conditions

Interrupt	CM02 = 0	CM02 = 1
Serial interface interrupt	Usable when operating with internal or external clock	Usable when operating with external clock
(Omission)	(Omission)	(Omission)
INT0 to INT2 interrupt	Usable	Usable (INT0 can be used if there is no filter.)
INT3 interrupt	Usable	Usable (Can be used when there is no filter and interrupt request is generated at INT3 (the TCC06 bit in the TCC0 register is set to "1"))
(Omission)	(Omission)	(Omission)