

RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-16C-A188A/E	Rev.	1.00
Title	M16C/65 Group, M16C/64A Group Easing the Limitation of the Debugging Interface		Information Category	Technical Notification		
Applicable Product	M16C/65 Group, M16C/64A Group	Lot No.	Reference Document			

A 1-wire debugging interface is now available for the E8a emulator.

1. Applicable Products

M16C/65 Group and M16C/64A Group MCUs produced on or after the 25th week in 2010.

Limited to the following part numbers in M16C/65 Group (xxx are the last three letters in the part number):

R5F36506xxx, R5F3651Exxx, R5F3650Exxx, R5F3651Kxxx, R5F3650Kxxx,

R5F3651Mxxx, R5F3650Mxxx, R5F3651Nxxx, R5F3650Nxxx

Check the product data code for production year and week. Refer to 3. Date Code.

2. Contents

Previously, the interface between the E8a emulator and MCU was a 2-wire clock asynchronous serial I/O. A 1-wire clock asynchronous serial I/O has been added.

Therefore, 1-wire clock asynchronous serial I/O can be selected for the products listed above. Refer to the following documents for more information on the connection method:

E8a Emulator Additional Document for User's Manual R0E00008AKCE00EP46

Renesas Microcomputer Development Environment System M16C Family / M16C/60 Series

Notes on Connecting the M16C/64A and M16C/65

3. Date code

The date code is listed in the marking of the package. Figure 1 shows the marking diagram.

For example, "025xxxx" indicates the 25th week of 2010.

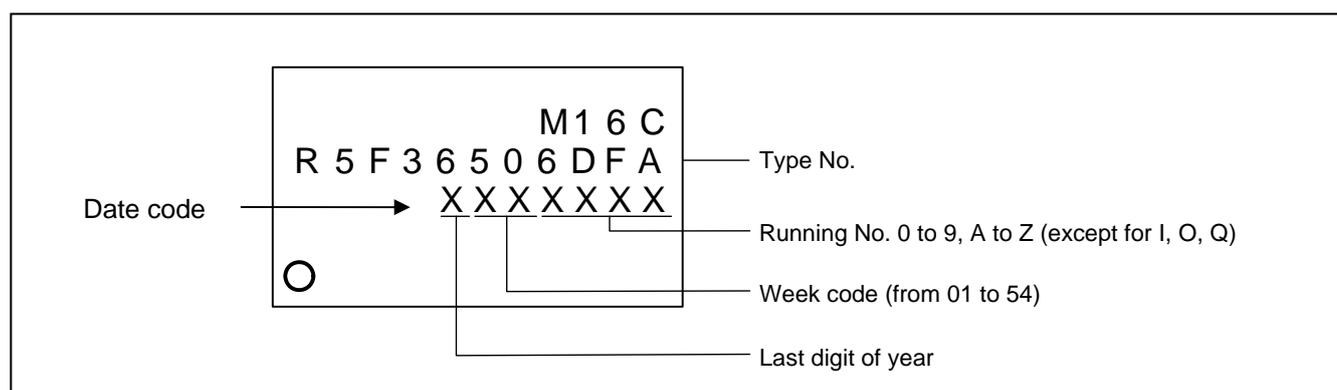


Figure 1: Marking Diagram