

RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-SH7-A851A/E	Rev.	1.00
Title	Notes about count mode setting for stopping output coded data of Jpeg Codec Unit		Information Category	Technical Notification		
Applicable Product	SH7268 Group SH7269 Group	Lot No.	Reference Document	SH7268 Group, SH7269 Group User's Manual: Hardware Rev1.00 (R01UH0048EJ0100)		
		All				

We would like to inform you of the notice about count mode setting for stopping output coded data of Jpeg Codec Unit.

【Notice】

When count mode setting for stopping the output of coded data is on(JIFECNT.JOUTC=1) for compression, compresses over the number of data set by JIFEDDC register. It occurs when bus load of output data destination is high.

Number of exceeding data depends on the timing of internal bus timing.

【Image】



【Workaround】

We delete count mode setting for stopping output coded data from the specification as there is no valid countermeasure.

When operating divisional process while JPEG compression, use divisional process from count mode setting for stopping input image data lines instead of count mode setting for stopping output coded data.