

RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-SH7-A852A/E	Rev.	1.00
Title	Notes about using the OpenVG™ graphics processor and JPEG codec unit at the same time		Information Category	Technical Notification		
Applicable Product	SH7268 Group SH7269 Group	Lot No.	Reference Document	SH7268 Group, SH7269 Group User's Manual: Hardware Rev1.00 (R01UH0048EJ0100)		
		All				

We would like to inform you of the notice about using the OpenVG™ graphics processor and JPEG codec unit at the same time.

【Notice】

When OpenVG™ graphics processor and JPEG codec unit use the internal bus simultaneously, JPEG codec unit may access unexpected address.

【Workaround】

When using the OpenVG™ graphics processor and JPEG codec unit at the same time, it is necessary to implement exclusive bus access control in software to ensure that they do not access the bus simultaneously.