

[Notes]

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Rev.1.00

Dec. 01, 2023

PG-FP6 Flash Memory Programmer

(Note on Reading Memory in Products of the RA Family)

Outline

When using the PG-FP6 flash memory programmer, note the following point.

1. Note on reading memory in products of the RA family

1. Note on Reading Memory in Products of the RA Family

1.1 Applicable Products

Programming GUI for PG-FP6 (FP6 Terminal) V1.08.00, V1.09.00, V1.10.00

1.2 Applicable Devices

RA family RA2A1 group: R7FA2A1AB

RA family RA2E1 group: R7FA2E1A5, R7FA2E1A7, R7FA2E1A9

RA family RA2E2 group: R7FA2E2A3, R7FA2E2A5, R7FA2E2A7

RA family RA2E3 group: R7FA2E305, R7FA2E307

RA family RA2L1 group: R7FA2L1A9, R7FA2L1AB

RA family RA4E1 group: R7FA4E10B, R7FA4E10D

RA family RA4M1 group: R7FA4M1AB

RA family RA4M2 group: R7FA4M2AB, R7FA4M2AC, R7FA4M2AD

RA family RA4M3 group: R7FA4M3AD, R7FA4M3AE, R7FA4M3AF

RA family RA4W1 group: R7FA4W1AD

RA family RA6E1 group: R7FA6E10D, R7FA6E10F

RA family RA6M1 group: R7FA6M1AD

RA family RA6M2 group: R7FA6M2AD, R7FA6M2AF

RA family RA6M3 group: R7FA6M3AF, R7FA6M3AH

RA family RA6M4 group: R7FA6M4AD, R7FA6M4AE, R7FA6M4AF

RA family RA6M5 group: R7FA6M5AG, R7FA6M5AH, R7FA6M5BF, R7FA6M5BG, R7FA6M5BH

RA family RA6T1 group: R7FA6T1AB, R7FA6T1AD

RA family RA6T2 group: R7FA6T2AB, R7FA6T2AD, R7FA6T2BB, R7FA6T2BD

1.3 Details

If memory in the target device is read by specifying a 2-Kbyte or larger range during SWD communications, correct data cannot be read from the 2-Kbyte or larger area.

Note that the same problem does not apply to the execution of a verify command during SWD communications or the reading of memory during UART communications.

Example: When the [Read Memory] command is executed with the start address and the end address specified as 0x00000000 and 0x00000FFF, correct data can be read in the range from 0x00000000 to 0x000007FF but cannot be read in the range from 0x00000800 to 0x00000FFF.

1.4 Workaround

Read memory in one of the following ways.

- (1) Divide the area into 2-Kbyte or smaller ranges.
- (2) Use a 2-wire UART connection.

1.5 Schedule for Fixing the Problem

This problem will be fixed in Programming GUI for PG-FP6 (FP6 Terminal) V1.11.00.

(Scheduled to be released on Jan. 22nd, 2024)

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Dec.01.23	-	First edition issued

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