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The Device File Updater Specific to the C Compiler Package for the R32C MCU Series Revised to V.1.03

We have revised the device file updater specific to the C compiler package for the R32C MCU series from V.1.02 to V.1.03.

For an overview of device file updaters, see:

<https://www.renesas.com/dfu>

The above URL is one of our global sites.

1. Supporting C Compiler

The C compiler package for the R32C series V.1.01 Release 00 and later

2. Descriptions of Revision

The following improvements have been made to the functionality:

- (1) The R32C/102, /145, /116A, /117A, and /118A CPUs have been added to the list of CPU types.
- (2) In the sfr files for all the CPUs listed, (a) how to prevent multiple inclusion has been explained and (b) the far area has been replaced with "_far" so that you can use the -fansi option.
- (3) In the sfr files for the R32C/151, /152, /153, /156, and /157 CPUs, the errors in the definition of the registers related to E2PROM Emulation Data Flash have been rectified.
- (4) In the intprg.c files for the R32C/116, /117, and /118, the names of some interrupt functions have been modified to the same that are used in the hardware manual of these CPUs.

3. How to Obtain the Revised Product

Download the device file updater V.1.03 specific to the C compiler package for the R32C MCU series from

https://www.renesas.com/dfu_download

The above URL is one of our global sites.

and then install it. This Web site will be opened from September 24.

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