

RENESAS TOOL NEWS on June 16, 2010: 100616/tn1

## The Device File Updater Used for the C Compiler Package for the R32C MCU Series Revised to V.1.02

We have revised the device file updater used for the C compiler package for the R32C MCU series from V.1.01 to V.1.02.

For an overview of Renesas device file updaters, go to:

<https://www.renesas.com/dfu>

The above URL is one of our global sites.

---

### 1. Compliant Environment

This device file updater operates in combination with the C compiler package for the R32C MCU series V.1.01 Release 00 or later.

### 2. Descriptions of Revision

The following CPUs can be added to the CPU Types list:

- R32C/120 and R32C/160

In addition, the sample files for the following CPUs have been modified:

- R32C/121 and R32C/161

The descriptions of modification are as follows:

(1) sfr file

- The types of structures have been unified into "unsigned".
- In the R32C/121, bit symbol FCA has been introduced to the FMR0 register.

(2) intprg.c file

The names of two interrupt functions are modified to those in the hardware manual as follows:

From            To

vect=39-41: \_bus\_collision\_\*\*() \_start\_stop\_condition\_\*\*()

vect=64-66: \_ssu\*()                    \_serial\_bus\_\*()

### 3. How to Obtain the Revised Product

Download the device file updater V.1.02 used for the C compiler package for the R32C MCU series from

[https://www.renesas.com/dfu\\_download](https://www.renesas.com/dfu_download)

and install it. This Web site will be opened from June 21.

The above URL is one of our global sites.

---

#### [Disclaimer]

The past news contents have been based on information at the time of publication. Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

© 2010-2016 Renesas Electronics Corporation. All rights reserved.