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Parameter Files for Flash Memory Programmer PG-FP5 Revised

We have revised parameter files specific to MCUs of the RL78 family and V850, which are used for the flash memory programmer PG-FP5.

1. Parameter Files and Versions Concerned

The following parameter files have been revised from V1.00 to V1.01:

In RL78 family MCUs

- PR5-R5F104 (for RL78/G14 group)

In V850 MCUs

- PR5-70F3569 (for V850E2/Fx4-H)
- PR5-70F4012 (for V850E2/Fx4)

2. Description of Revision

The following parameter files have been added to the above three parameter files:

- (1) PR5-R5F104 V1.01 (for RL78/G14 group)

The parameter files for the R5F104CE and the R5F104EE added.

- (2) PR5-70F3569 V1.01 (for V850E2/Fx4-H)

The parameter file for the UPD70F3561 added.

- (3) PR5-70F4012 V1.01 (for V850E2/Fx4)

The parameter file for the UPD70F3548 added.

For all the type names of supported MCUs, see "PG-FP5 target device list" in PDF (updated on this September 30) at:

http://www.renesas.com/pg_fp5

The above URL is one of our global sites.

3. Necessary Firmware, FPGA Data, and Programming GUI

The version numbers of and notes on firmware, FPGA data, and programming GUI necessary for you to use parameter files are described in the supplemental document (in PDF) included with each file. So you are encouraged to read it.

You can download the latest firmware, FPGA data, and programming GUI at:

http://www.renesas.com/pg_fp5_download

The above URL is one of our global sites.

4. Obtaining Parameter Files

Download the parameter file you want from the Web site (updated on this September 30) at:

http://www.renesas.com/pg_fp5_download

The above URL is one of our global sites.

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